

Theoretical and Experimental Analysis of Energy in Charging a Capacitor by Step-Wise Potential

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Abstract

In this paper, charging capacitor in RC circuit, to a final voltage, via arbitrary number of steps, is investigated and analyzed both theoretically and experimentally. The obtained results show that the stored energy in the capacitor is constant independent of N , but the dissipated energy in the resistor and the consumed energy by the power supply decreases as number of steps N increases (adiabatic charging). The limit when the step number goes to infinity is examined and our result shows that the dissipated energy vanishes theoretically. This limit is carried out experimentally by using a ramp potential.

Keywords

Charging Capacitor, RC Circuit, Stored Energy, Dissipated Energy, Adiabatic Charging

1. Introduction

Charging and discharging capacitors play important role in many applications of electrical energy storage [1] [2] [3]. During the charging process of a capacitor, the energy dissipation is usually of great concern [4] [5] [6] [7], so the efficient charging is a crucial factor and has been recently investigated [8] [9] [10]. Furthermore, reduction in energy dissipation has become one of the major concerns in communications and computing [11] [12] [13]. Recently, there has been more concern about energy storage and dissipation with the emergence of Graphene and other nanomaterials in recent development of supercapacitors [14] [15] [16] [17]. The usual treatment of charging a capacitor is the traditional R - C circuit, in which a capacitor of capacitance c and resistor of resistance R are connected in series with a power supply of constant voltage, V_0 . It is widely well-known that

the dissipated heat energy, Q in the resistor is equal to the energy stored, U in the capacitor when it is finally fully charged, namely

$$U = Q = \frac{1}{2}cV_0^2. \quad (1)$$

Therefore, the consumed energy by the power supply is divided equally between the stored energy in the capacitor and the dissipated energy regardless of the value of the resistance, R . It has long been recognized that abruptly charging the capacitor is accompanied by energy loss given by Equation (1). However, workers suggested gradual (adiabatic) charging to lower the energy loss [18] [19] [20] [21].

In this paper, we consider RC circuit in which the capacitor is charged up to a final potential V_0 through N steps. We derive the energy stored, the dissipation energy, and the consumed energy at the end of arbitrary j^{th} step. We also setup an experiment for this adiabatic charging and compare the theoretical derived quantities with the experimental ones. It is shown that, the final energy stored in the capacitor is a conserved quantity (given by Equation (1)), but the dissipated energy and the consumed energy by the power supply decrease as the number of steps increases. We also consider, theoretically, the special case, $N \rightarrow \infty$, in which case the dissipation energy vanishes and the consumed energy is just the energy stored in the capacitor. Experimentally, this special case is considered by using a ramp potential in the RC circuit and we show that the experimental results are in good agreement with the theoretical ones.

The structure of the manuscript is as follows: In Section 2, charging a capacitor in RC circuit using stepwise potential is theoretically examined and analyzed, so as the dissipation energy and the consumed energy are expressed in terms of the number of steps. In Section 3, the charging process of the capacitor in RC circuit is carried out experimentally using N steps potential, so the final charge on the capacitor is measured as the area under the current-time curve for each N . This enables us to compute the stored and the consumed energies, so the dissipation energy is computed from the difference between the two. Section 3 is devoted for conclusions and discussion.

2. RC with N Steps Charging: Theoretical Analysis

We consider RC in which a capacitor of capacitance c is connected in series to a resistor of resistance R and a power supply. The procedure is to charge the capacitor to a final potential V_0 through N steps. So in the process, the potential is carried out as

$$V = \frac{V_0}{N}, \frac{2V_0}{N}, \frac{3V_0}{N}, \dots, \frac{jV_0}{N}, \dots, V_0. \quad (2)$$

So considering the j^{th} step, the potential of the power supply is jV_0/N and thus Kirchhoff law yields

$$j \frac{V_0}{N} - \frac{q}{c} - iR = 0, \quad (3)$$

whose solution gives the charge on the capacitor during the j^{th} step;

$$q_j(t) = \frac{V_0 c}{N} [j - e^{-t/Rc}]. \quad (4)$$

So the final charge at the end of the j^{th} step is $q_j = \frac{jV_0 c}{N}$. It is noticed that the amount of charge during each step is the same, which can be seen from $q_j - q_{j-1}$ which is clearly equals to $V_0 c/N$.

The heat energy dissipated in the resistor during the j^{th} step is:

$$Q_{\text{during } j} = \int_0^\infty i^2 R dt = R \int_0^\infty \left(\frac{V_0}{NR} \right)^2 e^{-2t/Rc} dt = \frac{1}{2} c \frac{V_0^2}{N^2}, \quad (5)$$

where the current, $i = \frac{dq_j}{dt}$ is found from Equation (4). It is clear that the dissipated heat energy is independent of the step number, and thus it is the same for each step. Therefore, the accumulated dissipated heat energy at the end of the j^{th} step, Q_j is

$$Q_j = j Q_{\text{during } j} = \frac{1}{2} c \frac{V_0^2}{N^2} j, \quad (6)$$

and therefore, at the end of the final step ($j = N$), the total dissipated heat energy is given by

$$Q = \frac{1}{2} c \frac{V_0^2}{N}. \quad (7)$$

This shows that Q decreases as N increases and thus the dissipated heat energy vanishes as $N \rightarrow \infty$.

Now we consider the energy stored in the capacitor during this process:

At the end of the j^{th} step, the potential $V_j = jV_0/N$, so the stored energy is

$$U_j = \frac{1}{2} c V_j^2 = \frac{1}{2} c \left(\frac{jV_0}{N} \right)^2. \quad (8)$$

This can be proved by mathematical induction as follows;

$$1) \text{ For } j=1, U_1 = \int V dq = \int_{q_0}^{q_1} \frac{q}{c} dq = \frac{1}{2c} q^2 \Big|_{q_0}^{q_1}.$$

$$\text{But, } q_0 = 0 \text{ and } q_1 = \frac{V_0}{N} c. \text{ Therefore } U_1 = \frac{1}{2} c \left(\frac{V_0}{N} \right)^2.$$

2) We assume it is true at j and need to show it is true at $j+1$.

Energy stored between steps j and $j+1$ is

$$U_{j,j+1} = \int_{q_j}^{q_{j+1}} \frac{q}{c} dq = \frac{1}{2} c \left(\frac{V_0}{N} \right)^2 [(j+1)^2 - j^2].$$

But the stored energy at end of the $(j+1)^{\text{th}}$ step is

$$U_{j+1} = U_j + U_{j,j+1} = \frac{1}{2} c \left(\frac{V_0}{N} \right)^2 [j^2 + (j+1)^2 - j^2].$$

$$\text{So, } U_{j+1} = \frac{1}{2} c \left(\frac{V_0}{N} \right)^2 (j+1)^2.$$

That completes the proof, therefore the energy stored in the capacitor at the end of the j^{th} step is: $U_j = \frac{1}{2}c\left(\frac{V_0 j}{N}\right)^2$, So that when $j = N$, it gives $U = \frac{1}{2}cV_0^2$, which shows that the energy stored in the capacitor is a conservative quantity, *i.e.* it depends on the potential difference between the initial and final states. It is interesting to note, by comparing Equation (6) and Equation (8), that at the end of the j^{th} step $U_j \geq Q_j$. The equality sign holds only for $j = 1$.

Finally, we consider the consumed energy by the power supply (input energy):

This can be achieved by two methods: In the first, we calculate the work done by the power supply throughout the whole process and, in the second, we add the energy stored in the capacitor to the dissipation energy.

For the first method: We compute the input energy for each step and then we sum to the required step (say the j^{th} step). Letting $E_{i-1,i}$ be the input energy between the $(i-1)^{\text{th}}$ step and the i^{th} step, one may write

$$E_{i-1,i} = q_{i-1,i} V_i = \left(\frac{cV_0}{N}\right) \left(\frac{iV_0}{N}\right) = \frac{cV_0^2}{N^2} i. \quad (9)$$

Therefore, the input energy, E_j up to the j^{th} step is given by

$$E_j = \sum_{i=0}^j E_{i-1,i} = \frac{cV_0^2}{N^2} \sum_{i=0}^j i = \frac{cV_0^2}{N^2} \frac{j(j+1)}{2}. \quad (10)$$

The total input energy, E is obtained by substituting, $j = N$, with the result;

$$E_j = \frac{cV_0^2}{2} \frac{N+1}{N}. \quad (11)$$

Obviously, in the limit $N \rightarrow \infty$, Equation (11) gives $E = \frac{1}{2}cV_0^2$ which is equal to the energy stored in the capacitor. This is expected since, in this case, the dissipation energy vanishes.

For the second method: At the end of the j^{th} step, the input energy is the sum of the energy stored in the capacitor and the dissipation energy, which with the help of Equation (6) and Equation (8), one gets

$$E_j = U_j + Q_j = \frac{1}{2}c\left(\frac{V_0 j}{N}\right)^2 + \frac{1}{2}c\frac{V_0^2}{N^2} j. \quad (12)$$

The above equation can be simply written as

$$E_j = \frac{1}{2}c\frac{V_0^2}{N^2} j(j+1), \quad (13)$$

which is exactly the same as Equation (10). It reproduces the total input energy, given by Equation (11), when $j = N$.

3. RC with N Steps Charging: Experimental

In this section, we consider RC circuit with applied voltage that can be set at any number of steps, N to achieve a final voltage V_0 . The potential steps was applied

using computerized PGZ 402 universal potentiostat (radiometer analytical). Connection to RC circuit was established using two electrode connections mode as shown in **Figure 1**.

The procedure goes as follows: The voltage source is set at the required number of steps with duration time for each step much greater than the time constant (RC) of the circuit. The current in the circuit is plotted as function of time. The total charge, q delivered by the power source and accumulated at the capacitor is calculated by finding the area under the current-time curve. The final energy stored in the capacitor is calculated from the formula, $U = q^2/2c$. The input energy (consumed energy) is calculated as follows:

$$E = qV_{avg}, \tag{13}$$

where V_{avg} is the average potential the process of N steps and is given by

$$V_{avg} = \frac{1}{N} \sum_{j=1}^N \frac{V_0}{N} j = \frac{V_0}{N^2} \sum_{j=1}^N j. \tag{14}$$

Using $\sum_{j=1}^N j = N(N+1)/2$, Equation (14) becomes

$$V_{avg} = V_0 \frac{N+1}{2N}, \tag{15}$$

so the input energy becomes

$$E = qV_0 \frac{N+1}{2N}. \tag{16}$$

The dissipated energy is calculated from the difference between E and U , namely

$$Q = E - U = qV_0 \frac{N+1}{2N} - \frac{q^2}{2c}. \tag{17}$$

The experimental measurements were taken for different values of the number of steps, N . The special case $N \rightarrow \infty$ was considered by applying a ramp potential from the power source. For each value of N , a graph for current vs. time is obtained and these graphs are shown in **Figures 2-12**. Each contains the stepwise potential for the given value of the step number, N . **Figure 2**, for $N = 1$, represents the conventional charging capacitor by a constant voltage source. The current for any value of the step number N can be found from Equation (4), namely

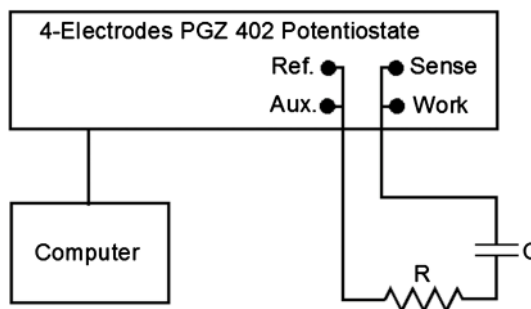


Figure 1. Schematic diagram for RC circuit connected to PGZ 402 Universal Potentiostat using two electrode mode connection.

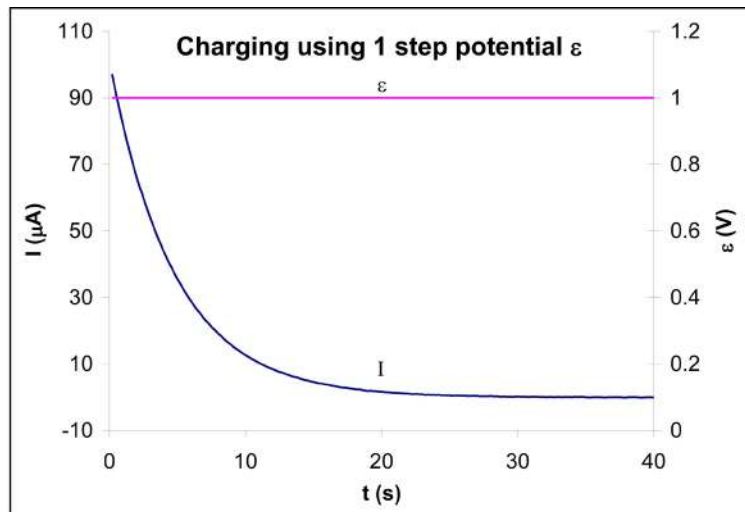


Figure 2. I vs. t for abrupt potential ($N=1$).

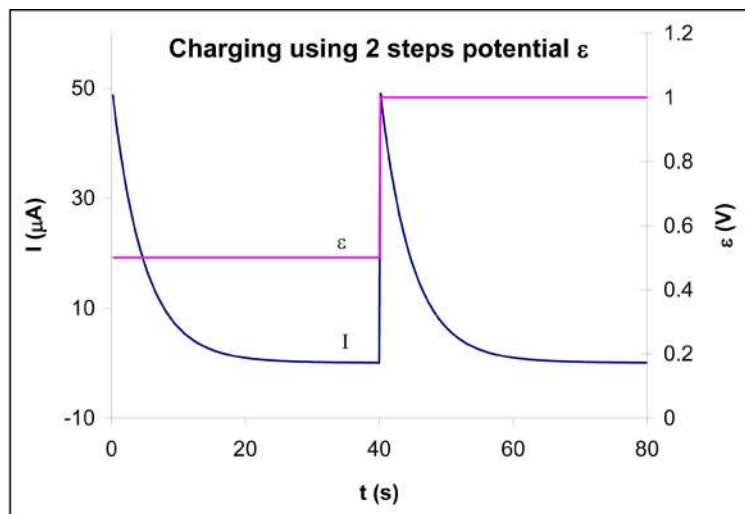


Figure 3. I vs. t for step potential ($N=2$).

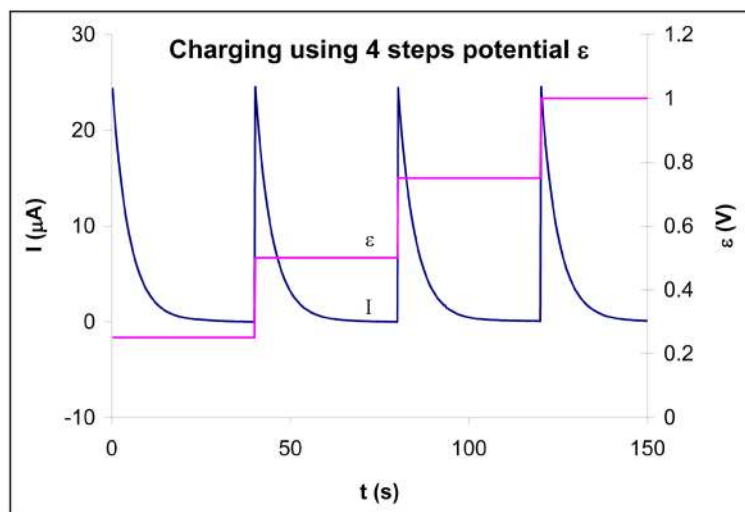


Figure 4. I vs. t for step potential ($N=4$).

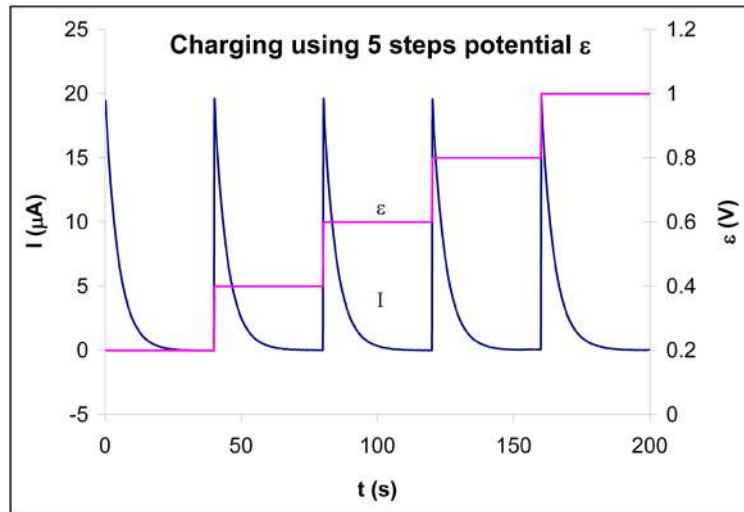


Figure 5. I vs. t for step potential ($N=5$).

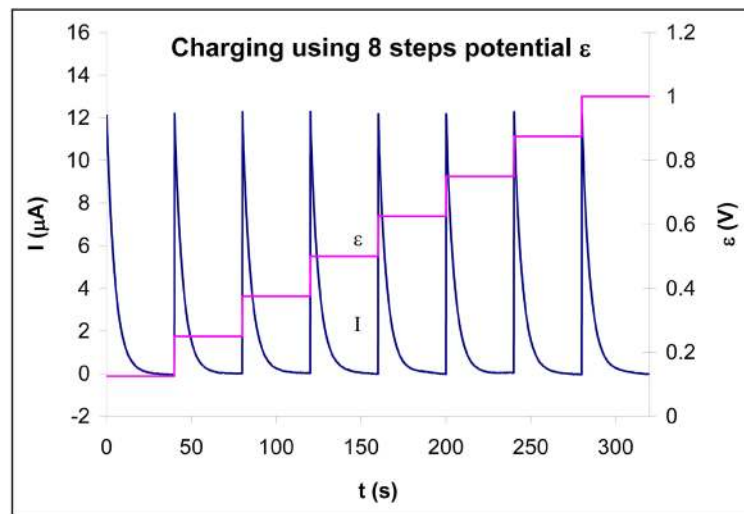


Figure 6. I vs. t for step potential ($N=8$).

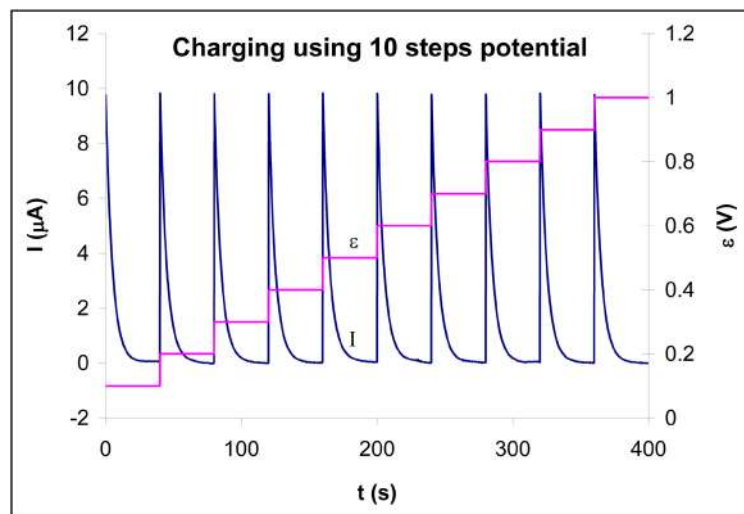


Figure 7. I vs. t for step potential ($N=10$).

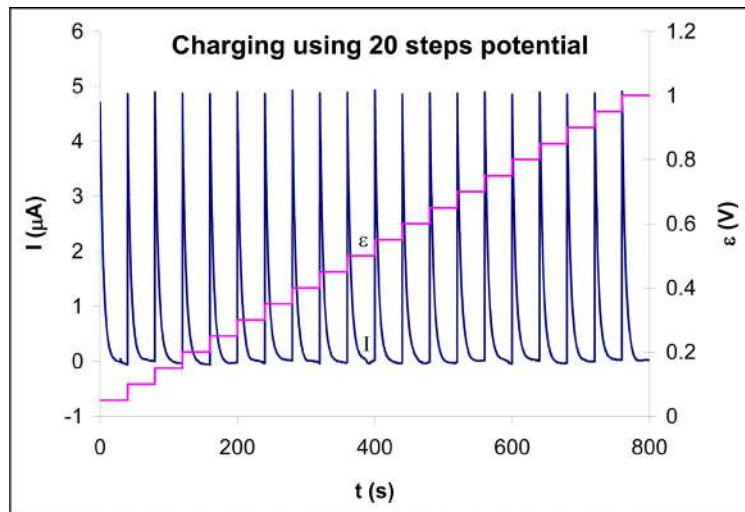


Figure 8. I vs. t for step potential ($N=20$).

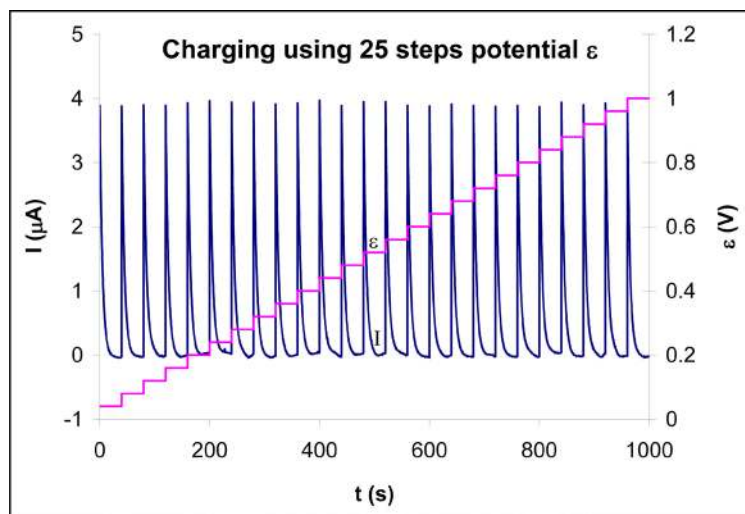


Figure 9. I vs. t for step potential ($N=25$).

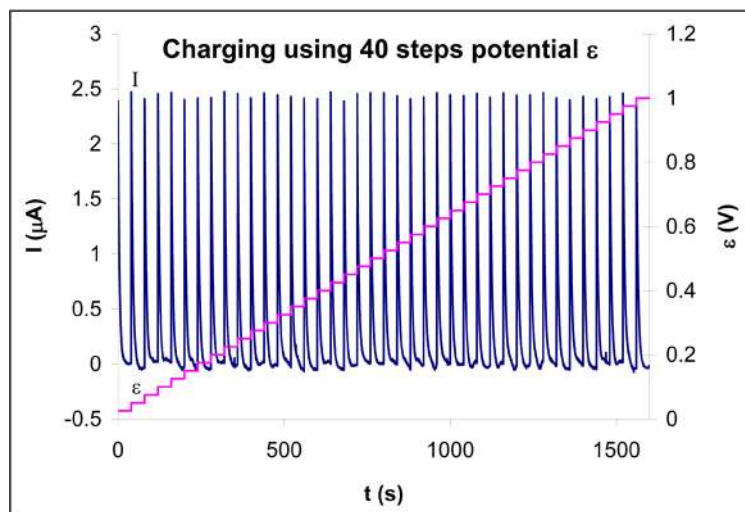


Figure 10. I vs. t for step potential ($N=40$).

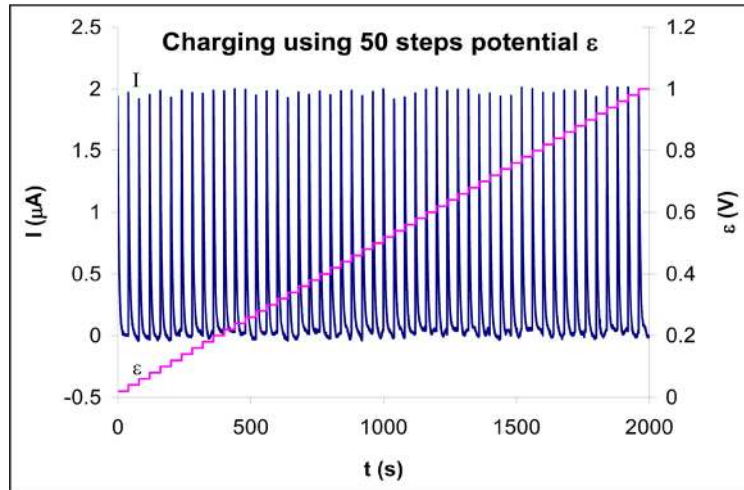


Figure 11. I vs. t for step potential ($N=50$).

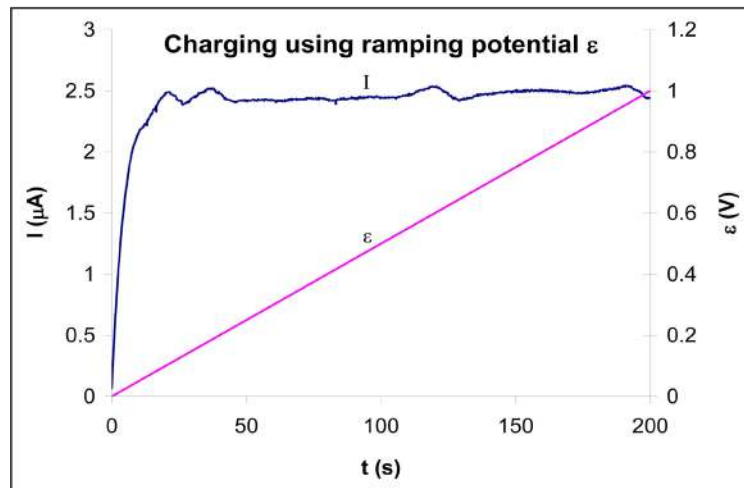


Figure 12. I vs. t for ramp potential ($N=\infty$).

$$i(t) = \frac{dq}{dt} = \frac{V_0}{RN} e^{-t/Rc} . \tag{18}$$

At the beginning of each step ($t = 0$), the initial current, $i_0 = V_0/RN$. Using the values, $V_0 = 1$ V, and $R = 10^4 \Omega$, the initial current for each step becomes $i_0 = \frac{100}{N} \mu\text{A}$. This value is in agreement with the experimental value for each step number in all stepwise potentials. **Figure 12** represents the current-time curve for the ramp potential which corresponds to the special stepwise case, $N \rightarrow \infty$. In order to explain this case, we derive below the current as function of time when the power source is a ramp potential. Consider the ramp potential $V(t) = \frac{V_0}{\tau} t$, where $\tau (= Rc)$ is the time constant of the RC circuit. Applying Kirchhoff law to the circuit gives us

$$\frac{q}{c} + iR = \frac{V_0}{\tau} t , \tag{19}$$

which can be written as

$$\frac{dq}{dt} + \frac{1}{Rc}q = \frac{V_0}{R\tau}t. \quad (20)$$

The above equation is a linear, inhomogeneous, first-order differential equation, which has the same structure as the standard form, namely

$$\frac{dx}{dt} + p(t)x = f(t). \quad (21)$$

The well-known method to solve the above equation is by the integrating factor method. In this method, defining an integrating factor $u(t) = e^{\int p(t)dt}$, then the solution for Equation (21) is

$$x(t) = \frac{1}{u(t)} \left(\int u(t)f(t)dt + A \right), \quad (22)$$

where A is a constant to be found from the initial condition. Applying this method to our differential Equation (21), we get $u(t) = e^{t/Rc}$, $f(t) = \frac{V_0}{R\tau}t$, so that the integral in Equation (22), gives (after integration by parts)

$$\int \frac{V_0}{R\tau}te^{t/Rc}dt = \frac{V_0}{R\tau} \left[Rct - (Rc)^2 \right] e^{t/Rc}. \quad (23)$$

Therefore, with $x(t) \rightarrow q(t)$, the solution of Equation (20) is

$$q(t) = \frac{cV_0}{\tau}t - \frac{RV_0c^2}{\tau} + Ae^{-t/Rc}. \quad (24)$$

Since the capacitor is initially uncharged, so $q = 0$ at $t = 0$ determines the constant A , and the substitution, $\tau = Rc$, enables us to write Equation (24) as

$$q(t) = \frac{V_0}{R}t - cV_0(1 - e^{-t/Rc}). \quad (25)$$

The electric current is now readily obtained, using $i = dq/dt$, with the result of

$$i = \frac{V_0}{R}(1 - e^{-t/Rc}). \quad (26)$$

Therefore, after a long time $t \gg \tau$, the electric current attains a constant value, V_0/R . This explains the plateau behavior in **Figure 12**. The almost linear behavior of the current for very short time $t \ll \tau$ in **Figure 12** can be explained by expanding the exponential in Equation (26) and retaining the first two terms to get $i \rightarrow \frac{V_0}{R^2c}t$, which is linear in time.

Chrono-coulometry experiment has been performed in order to determine the capacitance best value, c of the used commercial capacitor. In this method, a constant voltage, $V_0 = 1$ V is applied and then the charge accumulated on the capacitor is plotted as function of time as shown in **Figure 13**.

The maximum stored charge in the capacitor, $q = 478.24$ μ C (measured when the current is close to zero as the area under the corresponding current-time

curve. Therefore, the capacitance, $c = \frac{q}{V_0} = \frac{478.24 \mu\text{C}}{1 \text{ V}} = 478.24 \mu\text{F}$. The experimental and theoretical values of the stored energy are respectively

$$U_{\text{exp}} = \frac{q^2}{2c}, U_{\text{th}} = \frac{V_0^2}{2C}, \tag{27}$$

while the experimental and theoretical values of the dissipated energy and the input energy are calculated from Equations ((17), (7), (16), and (11)) respectively. Our experimental and theoretical results are tabulated in **Table 1**.

Our results show a good agreement between the experimental and theoretical values. For comparison purposes, we compute the ratios between both the stored energy and the dissipated energy on one hand, and the input energy on the other for the experimental and the theoretical values. These quantities are shown in **Table 2**.

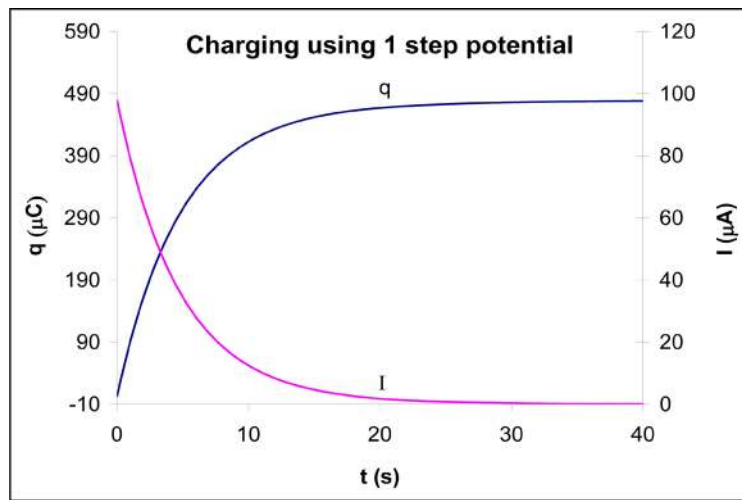


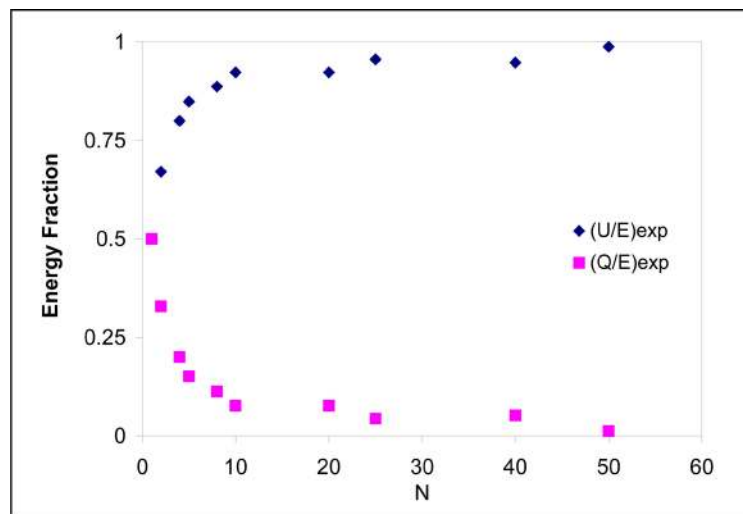
Figure 13. The charge and current as function of time to find the capacitance, c .

Table 1. Experimental (*exp*) & theoretical (*th*) results for input energy (E), stored energy (U), and dissipated energy (Q) for different values of the step number N .

N	q (μC)	E_{exp} (μJ)	E_{th} (μJ)	U_{exp} (μJ)	U_{th} (μJ)	Q_{exp} (μJ)	Q_{th} (μJ)
1	478.24	478.24	478.12	239.12	239.12	239.12	239.12
2	481.2	360.90	358.68	242.09	239.12	118.81	119.56
4	478.3	298.93	298.90	239.18	239.12	59.75	59.78
5	487.44	292.46	286.94	248.41	239.12	44.05	47.82
8	477.17	268.41	269.01	238.05	239.12	30.35	29.89
10	485.77	267.17	263.03	246.71	239.12	20.47	23.91
20	463.5	243.34	251.07	224.61	239.12	18.74	11.96
25	475.51	247.26	248.68	236.39	239.12	10.87	9.56
40	464.86	238.24	245.10	225.93	239.12	12.31	5.98
50	482.39	246.02	243.90	243.28	239.12	2.74	4.78
∞	481.41	240.71	239.12	242.30	239.12	-1.59	0

Table 2. Stored energy and dissipated energy ratios relative to input energy, both experimentally and theoretically.

N	$\left(\frac{U}{E}\right)_{exp}$ (%)	$\left(\frac{U}{E}\right)_{th}$ (%)	$\left(\frac{Q}{E}\right)_{exp}$ (%)	$\left(\frac{Q}{E}\right)_{th}$ (%)
1	50	50	50	50
2	67.1	66.7	32.9	33.3
4	80	80	20	20
5	84.9	83.3	15.1	16.7
8	88.7	88.9	11.3	11.1
10	92.3	90.9	7.7	9.1
20	92.3	95.2	7.7	4.8
25	95.6	96.2	4.4	3.8
40	94.8	97.6	5.2	2.4
50	98.8	98.0	1.2	2
∞	100.6	100	-0.6	0

**Figure 14.** Experimental ratios of both the stored and dissipated energy relative to the input energy.

It is noticed that we achieved a very high percentage for the stored energy ratio, and thus a very low percentage for the dissipation energy ratio. We show in **Figure 14**, the experimental ratios between the energy stored and the dissipated energy relative to the input energy by the power source.

4. Discussion and Conclusion

In this paper, we investigated the stepwise charging of a capacitor in RC circuit. The potential step was applied using the PGZ 402 universal potentiostat. In the first part, theoretical investigation is carried out: Analytical results for the dissipation energy and the consumed energy were derived when charging the capacitor by N steps to achieve the final voltage. It was shown that each of the energy

dissipation and the consumed energy decreases as the step number N increases, while the energy stored in the capacitor is independent of N . An interesting result is that the dissipation energy vanishes as $N \rightarrow \infty$ and in this case the consumed energy is equal to the stored energy in the capacitor. In the second part, experimental analysis is investigated: This has been carried out by plotting current vs. time in each case and the charge on the capacitor is computed from the area under the curve. The calculated charge was used to find the energy stored in the capacitor and the use of Equations (16) and (17) yields the dissipated energy and the consumed energy. Our theoretical results were verified by carrying experiment measurements of the consumed energy, stored energy and the dissipated energy using stepwise potential and the ramp potential that corresponds to the case $N \rightarrow \infty$. Our experimental results were found to be in good agreement with the theoretical ones.

Conflicts of Interest

The authors declare no conflicts of interest regarding the publication of this paper.

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List of Symbols and Variables with Their Definitions

- R , Resistance;
- C , Capacitance;
- N , Number of steps;
- V_0 , Power supply voltage;
- q , Charge on the capacitor;
- U , Stored energy in the capacitor;
- Q , Heat energy dissipated;
- E , Consumed energy;
- τ , Time constant.