

Thermal oxidation of nickel disilicide

M. Bartur and M-A. Nicolet

California Institute of Technology, Pasadena, California 91125

(Received 24 July 1981; accepted for publication 9 October 1981)

The thermal oxidation characteristics of nickel disilicide on Si substrates have been investigated in the temperature range of 700–900 °C in dry oxygen and wet oxygen. A surface layer of SiO₂ grows parabolically in time. The growth rate is independent on the crystalline structure (epitaxial or polycrystalline) and thickness of the NiSi₂ layer. We surmise that the oxidation mechanism is dominated by oxygen diffusion through the growing SiO₂. Activation energies for the dry and wet oxidation are 1.0 ± 0.1 eV and 1.5 ± 0.1 eV, respectively. NiSi₂ layers on SiO₂ oxidize with the same rate—resulting with progressively Ni-rich NiSi₂. Preliminary measurements of the oxide quality yield dielectric strength of 2.1×10^6 V cm⁻¹, and a pinhole density of about 100 per cm². A survey of oxidation data for Si and other refractory metal silicides shows that their oxidation does not draw similar kinetics to that of NiSi₂.

PACS numbers: 81.60.Bn, 68.55. + b, 82.20.Pm, 68.90. + g

Refractory metal silicides have been extensively explored as possible material for integrated circuit interconnects.^{1,2,3} Two of the most important desired properties for such a material are a low resistivity and the possibility to form a thermal oxide. The resistivity of NiSi₂ is 40–60 $\mu\Omega$ cm^{1,4} compared to 400 $\mu\Omega$ cm for highly doped poly-Si as is currently used. This makes NiSi₂ an attractive material for the desired purpose. The formation of an insulating layer of SiO₂ is important for isolation, passivation, and photolithography. In this context, it is therefore of interest whether NiSi₂ (not refractory, melting point 993 °C, lowest eutectic temperature 966 °C) can be oxidized. The eutectic temperature of 966 °C limits subsequent processing to temperature of about 900 °C. This limit is no hindrance, since the metallization step comes after the completion of all diffusion steps, and it is then desired to keep processing temperatures low in order to minimize further diffusion. In this letter, we explore the thermal oxidation of NiSi₂ and characterize the SiO₂ growth kinetics.

Nickel films were deposited onto $\langle 111 \rangle$ *n*-type Si substrates using a dry-pumped *e*-gun evaporator. The pressure during deposition was less than 2×10^{-7} Torr. The vacuum annealing was performed at a pressure less than 10^{-6} Torr at 400 °C for 30 min, and was followed by ramping the temperature up to 800 °C for 20 min; a further annealing at 800 °C for 15 min completed the film preparation by transforming all the Ni to NiSi₂. We confirmed by backscattering spectroscopy (BS) and channeling measurements that NiSi₂ layers thus formed were single crystalline in nature.⁵ Additional samples were prepared on poly-Si. For these samples, a thick (1600 Å) layer of amorphous silicon was initially *e*-gun evaporated at a pressure less than 3×10^{-7} Torr on $\langle Si \rangle$ substrates, followed by Ni deposition without breaking vacuum. These samples were then annealed in the way described above to form NiSi₂. Since the amorphous deposited Si turns into a polycrystalline structure while silicide grows, one expects the NiSi₂ layer to be polycrystalline. X-ray diffraction pattern of the NiSi₂ confirmed the polycrystalline nature of these layers.

The oxidation was performed under two different con-

ditions: dry O₂ atmosphere and wet O₂ (bubbled through 95 °C H₂O). The oxide film was measured by 2-MeV ⁴He⁺ BS. These measurements gave a stoichiometric ratio of 1 to 2 (within the accuracy level of BS measurement) for Si to oxygen, indicating that the oxide is SiO₂. The SiO₂ thickness was calculated, assuming an SiO₂ density of 2.27 g cm⁻³.⁶ The use of 2-MeV He enables us to assume the “classical” vapor values of stopping power⁷ of an oxygen atom for our energy range.⁸

The first issue we address is the dependence, if any, of the oxidation process on the NiSi₂ film thickness. Samples with Ni thicknesses of 500 and 1400 Å were prepared on Si $\langle 111 \rangle$ substrates, and with 750-Å Ni thickness on 1600 Å amorphous on $\langle Si \rangle$. The samples were then thermally transformed to NiSi₂ layers with thicknesses of 1750, 5000, and 2650 Å, respectively. These samples were oxidized at 900 °C for a duration of 15 min to 2 h in wet oxidation, and for 1 h up to 12 h in dry oxidation.

All samples, regardless of their initial NiSi₂ thickness, grew exactly the same thickness of SiO₂ for a given time of oxidation. This fact suggests that the growth is controlled by the SiO₂, either in the bulk (diffusion) or at the interfaces (reactions), or both, but not by transport across the silicide layer. The fact that samples prepared on poly-Si or on $\langle Si \rangle$ have the same oxidation rate also shows that the structure and orientation of the substrate Si at the Si/NiSi₂ interface is of no consequence.

Since the main interest in NiSi₂ films for integrated circuits is for interconnections, we evaporated Ni on thin poly-Si which was deposited on SiO₂. This situation is close to real usage in integrated circuits. About 2400 Å of poly-Si were *e*-gun evaporated on SiO₂ substrate, followed by 500-Å-thick Ni layer. After the annealing step, one would expect 1850 Å of the poly-Si to be consumed by the interaction with the Ni, which would leave about 550 Å of unreacted poly-Si. We did not observe a sharp interface between NiSi₂ and the poly-Si, which is probably due to Ni diffusion into the poly-Si layer through grain boundaries. (The same observation was made for the polycrystalline samples on Si.) Again, the silicide formed was polycrystalline in nature compared to the single-

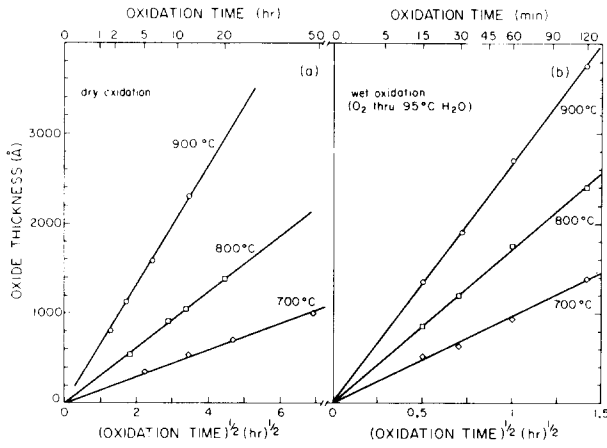


FIG. 1. Thickness of SiO_2 layer grown thermally on NiSi_2 on a $\langle\text{Si}\rangle$ substrate for (a) dry oxidation and (b) wet oxidation at 700, 800, and 900 °C as a function of time. The thickness depends parabolically on time.

crystal epilayer of NiSi_2 that form on $\langle\text{Si}\rangle$.⁵ These samples were then oxidized. The oxide thickness grown was exactly the same at 900 °C, wet or dry, for equal oxidation times, as that of all the other samples with different NiSi_2 thicknesses or crystalline structure. However, these new samples had a limited supply of Si underlying the layer. Once the oxidation process had consumed all the underlying Si, a change in the oxidation rate was anticipated. In our samples, this situation should occur for an oxide thickness above 1200 Å. For oxides thicker than that, we found from BS that the stoichiometric ratio in the NiSi_2 changed toward increasingly Ni-rich silicide. But measurements of the oxidation kinetics yielded a constant oxidation rate for an oxide thickness up to 3700 Å. At that oxide thickness, in addition to the remaining 550 Å of poly-Si, approximately 60% of the NiSi_2 layer was oxidized. This oxide growth was not accompanied by any

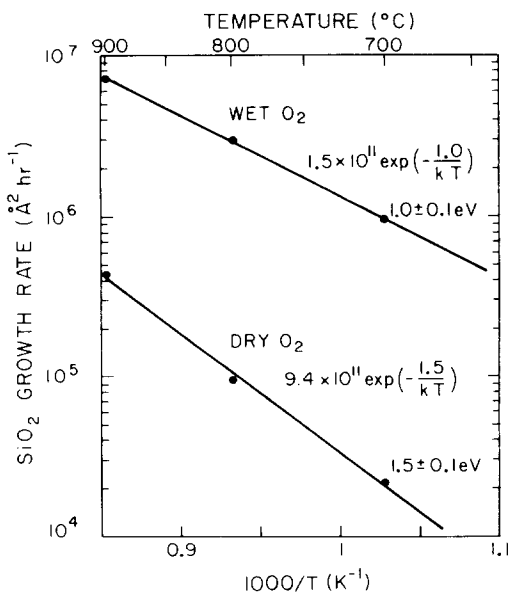


FIG. 2. Arrhenius plot of oxide growth rates on NiSi_2 for dry and wet oxidation.

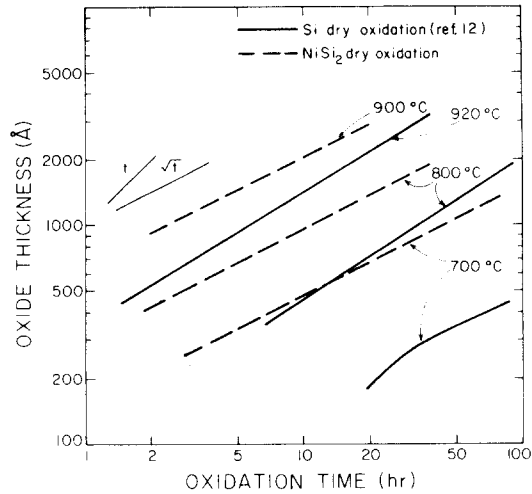


FIG. 3. Comparison between dry oxidation of $\langle\text{Si}\rangle$ and NiSi_2 .

detectable trace of NiO_x . The Ni-rich silicide thus oxidizes at the same rate as the stoichiometric NiSi_2 on excess silicon.

Having established the fact that the oxide growth is independent of the NiSi_2 thickness, structure, or even stoichiometric ratio of the silicide, enabled us to carry on the analysis of the kinetics with one specific Ni thickness. We used 500-Å Ni on $\langle 111 \rangle$ Si substrate for those oxidations that were performed at 800 and 700 °C under dry and wet conditions. Figure 1 summarizes the results of those oxidations under dry O_2 conditions (a), and wet oxidation (b). It is clear that the reaction has a parabolic time dependence (thickness is proportional to \sqrt{t}). The kinetics of the reaction can be fitted to the following equation:

$$d^2 = K_0 \exp(-E_a/kT)t, \quad (1)$$

where t is the growth time, d is the SiO_2 thickness, E_a is the activation energy, k is the Boltzmann constant, T the absolute temperature, and K_0 is a proportionality constant related to the physical properties of the oxidizing species and the structure of the oxide. Figure 2, which is an Arrhenius plot of the oxidation rates (derived from Fig. 1) shows that indeed, the above equation describes the oxidation process well and enables us to determine K_0 and E_a .

Preliminary measurements yield dielectric strengths of $2.10 \times 10^6 \text{ V cm}^{-1}$ and a pinhole density of about 100 cm^{-2} for a 4700-Å-thick NiSi_2 sample with 1200-Å SiO_2 . A pinhole was defined in this measurement as a localized defect which reduces the dielectric strength. Both values are compatible with integrated circuit requirements. Note that the pinhole density is for an oxide grown in a standard laboratory that is not equipped with special atmospheric control.

This initial study establishes the feasibility of using NiSi_2 as metal gate and interconnect in integrated circuit technology and characterizes the rates of the oxidation process. Other aspects, left open, that should be investigated further before practical implementation are the properties of a metal/ SiO_2 / silicon structure with NiSi_2 gate metallization, resistance to electromigration, capability to be defined into fine patterns, and chemical compatibility to integrated circuit processing; but our initial results look promising.

TABLE I. Oxidation characteristics of different silicides compared to that of silicon.

Temperature range (°C)			Si (Ref. 12) 700–1200	NiSi ₂ 700–900	TiSi ₂ (Ref. 15) 900–1050	TaSi ₂ (Ref. 18) 900–1050	WSi ₂ (Ref. 22) 1000–1200
Activation energy (eV)	Dry	linear	2.0	...			
		parabolic	1.24	1.5	no oxidation	no oxidation; oxidation ¹⁹	no oxidation
	Wet	linear	2.0	...	2.0
		parabolic	0.71	1.0	1.4	1.4	1.0 ^a
Oxide thickness (Å) 1 h at 900 °C-wet			2000	2700	2200	1200	2900 ^b

^aWSi₂ oxidation goes as $x^n \propto t$, where x is the oxide thickness, t is time, and n was found²² to be 1.8.

^bExtrapolated; error is about $\pm 15\%$.

We found that the growth is controlled by the SiO₂ (bulk and/or surfaces) and not by the silicide. The parabolic growth rate implies a diffusion-limited process. From oxidation studies of Si, it is known that the diffusing species through the oxide^{9–11} is oxygen (or oxygen-containing molecules). Since SiO₂ grows in our case too, we infer that the rate-limiting process is transport through the oxide, and that some oxygen-containing molecules are the moving species.

The oxidation kinetics of Si^{12,13} is described by a combination of linear and a parabolic rate constants. In Fig. 3, we compare the silicon dry oxidation data¹² with that of the NiSi₂. Clearly, the oxidation rate of NiSi₂ is higher due to the absence of a surface kinetics limitation (no linear regime). If there is an initial region where NiSi₂ oxidation obeys a linear time dependence, the corresponding SiO₂ thickness must be much smaller than that of the Si oxidation case. Furthermore, the parabolic rate constant for Si dry oxidation has an activation energy of 1.24 eV,¹² but that of NiSi₂ is 1.5 eV (see Table I). These differences and those observed also for wet oxidation (see Table I) suggest that the details of the diffusion may differ between Si and NiSi₂. Recent and detailed experimental investigations have established, however, that the dry oxidation of Si has a non-Arrhenius behavior and does not obey the linear-parabolic oxidation model exactly, so that the interpretation of activation energy values and their differences is not straightforward.¹⁴

It is of interest to compare the oxidation of NiSi₂ to that of other silicides. Such studies have been performed on TiSi₂,^{15,16} HfSi₂,¹⁷ TaSi₂,^{18,19} MoSi₂,²⁰ WSi₂,²¹ CoSi₂,¹⁰ and PtSi.¹⁰ Except for HfSi₂, all these silicides form an SiO₂ layer on top of the silicide under the appropriate oxidation conditions. It is reported that TiSi₂,¹⁶ TaSi₂,¹⁸ and WSi₂²² do not oxidize in dry oxygen environment even at temperatures of 1000 °C and above. A contradictory report states that TaSi₂ (Ref. 19) can be dry oxidized. In the TiSi₂ and TaSi₂ cases, metal oxides TiO_x and Ta₂O₅ were formed on top of the SiO₂. CoSi₂ was steam oxidized¹⁰ only at one temperature and parabolic growth rate was established.

The only detailed kinetics analysis reported is for Ti, Ta, and W disilicides. The results are summarized in Table I. We cannot explain the differences in the oxidation kinetics for the above silicides. It is apparent that NiSi₂ oxidation is not similar to either Si oxidation or the Refractory metal

silicides. We think that the diffusion species through the SiO₂ is likely to be the same in all cases. The observed differences then may reflect structural variations of the growing SiO₂ layer and/or additional diffusion processes for some silicides. As pointed out by Pliskin²³ stress also can affect the oxide rate. To fully understand the oxidation process, further investigations are needed.

The authors wish to thank R. Gorris and R. Fernandez for technical assistance. The work was executed under the U.R. Fund of the Böhmische Physical Society (B. M. Ullrich). Partial financial support by Solid State Devices, Inc. (A. Applebaum, President) is thankfully acknowledged as well.

¹S. P. Murarka, *J. Vac. Sci. Technol.* **17**, 775 (1980).

²F. Mohammadi, Ph.D. Thesis, Stanford University 1980.

³T. Mochizuki, K. Shibata, T. Inoue, and K. Ohuchi, *Jpn. J. Appl. Phys. Suppl.* **17-1**, 37 (1978).

⁴M. Mäenpää, (private communication).

⁵K. C. R. Chiu, J. M. Poate, L. C. Feldman, and C. J. Doherty, *Appl. Phys. Lett.* **36**, 544 (1980).

⁶A. S. Grove, *Physics and Technology of Semiconductor Devices* (Wiley, New York, 1967), p. 102.

⁷W. K. Chu, J. W. Mayer, and M-A. Nicolet, *Backscattering Spectrometry* (Academic, New York, 1978).

⁸J. F. Ziegler and W. K. Chu, *J. Appl. Phys.* **47**, 2239 (1976).

⁹A. S. Grove, *Physics and Technology of Semiconductor Devices* (Wiley, New York, 1967), p. 23.

¹⁰R. Pretorius, W. Strydom, and J. W. Mayer, *Phys. Rev. B* **22**, 1885 (1980).

¹¹D. R. Wolters, *J. Electrochem. Soc.* **127**, 2072 (1980).

¹²B. E. Deal and A. S. Grove, *J. Appl. Phys.* **36**, 3770 (1965).

¹³H. F. Wolf, *Silicon Semiconductor Data* (Pergamon, Oxford, 1976), p. 549.

¹⁴E. A. Irene and D. W. Dong, *J. Electrochem. Soc.* **125**, 1146 (1978).

¹⁵J. R. Chen and M. P. Houg, *Appl. Phys. Lett.* **37**, 824 (1980).

¹⁶J. R. Chen, Y. C. Liu, and S. D. Chu, Abstract from Technical Program of the Electronic Materials Conference, Santa Barbara, California, June 24–26, 1981.

¹⁷S. P. Murarka and C. C. Chang, *Appl. Phys. Lett.* **37**, 639 (1980).

¹⁸S. P. Murarka, D. B. Fraser, W. S. Lindenberger, and A. K. Sinha, *J. Appl. Phys.* **51**, 3241 (1980).

¹⁹K. C. Saraswat, R. S. Nowicki, and J. F. Moudler, Abstract from the Technical Program of the Electronic Materials Conference, Santa Barbara, California June 24–26, 1981.

²⁰T. Inoue and K. Koike, *Appl. Phys. Lett.* **33**, 826 (1978).

²¹S. Zirinsky, W. Hammer, F. d'Heurle, and J. E. E. Baglin, *Appl. Phys. Lett.* **33**, 76 (1978).

²²F. Mohammadi, D. C. Sarawat, and J. D. Meindl, *Appl. Phys. Lett.* **35**, 529 (1979).

²³W. A. Pliskin, *IBM J.* **10**, 198 (1966).