# Three-Phase Four-Switch Converter for SPMS Generators Based on Model Predictive Current Control for Wave Energy Applications

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Abstract This paper presents a model predictive current control (MPCC) for three-phase four-switch converters (TPFSC) connected to surface permanent magnet synchronous generators (SPMSGs) in oscillating water column (OWC) wave energy plants, that brings some benefits over the existing control methods used in this type of plants. The proposed MPCC for TPFSC follows the current references with great accuracy, whereas the switching frequency of the insulated-gate bipolar transistor (IGBTs) is fixed and low. This method minimizes the current reference tracking error, and its fast response makes it suitable for the power take-off systems present in wave energy converters. Furthermore, the system features a fast capacitor voltage offset suppression control. The dynamic performance and the voltage offset control of the proposed strategy for TPFSC feeding a SPMSG is evaluated in the Simulink environment. Later, experimental studies are carried out on an 8.7 kW laboratory SPMSG prototype. Finally, the capability of the proposed method to harvest the maximum energy from irregular waves is assessed using an OWC power plant emulator.

*Index Terms* Control system, emulation, oscillating water column (OWC), predictive control, wave energy.

## I. INTRODUCTION

O CEAN wave energy is one of the promising renewable energy sources which has attracted the attention of researchers, undergoing a fast develop during recent years [1]. Among the various types of wave energy converters (WEC), the oscillating water column (OWC) is the most improved technology and practical power-takeoff (PTO) system [2], [3], see Fig. 1. In this type of power plants, the pressure exerted by the waves inside a chamber produces an air flow that drives an air turbine connected to an electric generator. The OWC can be installed onshore and offshore. However, the onshore is preferred due to its easier maintenance, robust structure, and easier grid connection [4].

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The generator of this PTO system has to operate under a variable speed regime in order to harvest the maximum energy from the very large airflow variations [5]. Squirrel cage and doubly fed induction generators (DFIG) [6], [7], as well as permanent magnet synchronous generators (PMSG) [8], have been used in OWC-based power plants.

One recent example of a large-scale commercial OWC wave plant was installed recently in Mutriku, Spain, and the obtained power is nowadays injected into the grid [9]. A variable speed DFIG featuring vector controlled back to back converters is used in this 296 kW wave power plant to obtain the maximum energy from the waves. However, in this OWC power plant was necessary to tune the proportional integrator (PI) controllers and to know well the model of the plant in order to control the system correctly [10], [11]. Ocean waves are highly irregular and random so the airflow in the OWC chamber varies largely in few seconds. As a result, a robust speed control with a fast dynamic response is a vital part of the electrical generator control of any OWC plant. Consequently, nonlinear controls seem to be an appropriate solution. In [11], it is suggested to use a robust sliding mode control for this power plant in Mutriku in order to achieve better performances. Recently, the feasibility of employing a predictive control for an OWC based on DFIG for irregular waves has been reported [12]. Although the performance of the system seems to be increased, the experimental results were not reported.

PMSG is another interesting choice for OWC power plants due to its higher efficiency, especially for low power systems, and also because of the possibility to eliminate the gearbox, which increases the reliability of the system [13]. In [14], a nonlinear hysteresis control is adopted for the PMSG-based PTO. The results are promising, but this solution deals with a high and variable switching frequency, which implies a poor current waveform or the need of using bigger filters.

Another important matter that should be considered in WECs is that the system should continue working during some hardware faults such as the breakdown of a switch, in order to prevent losing potential power from the waves or to keep the control of the buoy in the case of floating OWCs to assure the buoy survival under storm conditions.

The three-phase four-switch converter (TPFSC) is one of the fault-tolerant solutions that can be applied when one of the six switches or one of the converter legs is damaged. In this

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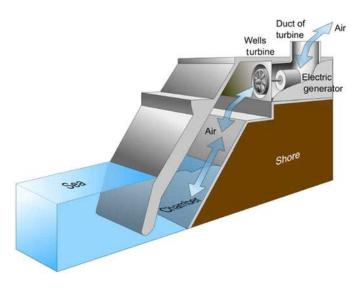


Fig. 1. Schematic representation of an OWC power plant.

case, the midpoint of the dc link should be connected to the faulty phase, while the rest four switches continue working [15], [16]. Although the converter can work with only four switches, unbalanced currents and high power ripples could appear in the system if the control is not properly designed for this fault situation [17].

TPFSC working as rectifiers, as shunt active filters, or feeding PMS motors, has been investigated during recent years [18]–[21]. In [21], a comprehensive torque ripple reduction is presented for TPFS feeding a PMSM, where a preferred space vector modulation (SVM) was proposed. The torque ripple that is induced by the voltage drop of the converter switches was eliminated by a compensation modulation strategy in the case of PMSM [22]. A hybrid SVM technique to reduce the torque ripple of a PMSM by using equivalent zero vectors is presented in [23]. This strategy determines the voltage sector location from the stator current position. In these last methods, the scope was focused on the modulation, while the current control strategy was not reported.

A predictive direct torque control for TPFS inverter was introduced to drive an induction machine in [24]. In this strategy, only one voltage vector that minimizes a cost function was employed at every switching period. Although the voltage deviation suppression was also included in the cost function using a weighting factor, the value of the corresponding factor had a big influence on the control, to the extent that the performance of the machine was deteriorated. Besides, the switching frequency was variable and the torque ripple was high. A model predictive current control (MPCC) for PMSM is presented in [25], introducing a four sectors division and a virtual duty cycle. According to the sector, one active voltage vector and the determined virtual duty cycle are employed. The effect of each voltage vector on the voltage deviation is studied in [26]. In order to remove the voltage offsets, it is necessary to extract the mean value of the capacitors which is usually done by using notch or low-pass filters [23], [26], [27]. This method was adopted in most of the research works to overcome the voltage offset. However, in this

technique, when the speed of the machine is low, the results are not good, and it presents a big delay when extracting the mean value.

To the best knowledge of the authors, TPFSC connected to SPMSG for wave energy applications has not been reported. This is an important case, since the inherent irregular waves can make the dc voltage offset to have big values if a fast offset compensation strategy is not adopted, especially during the low rotational speeds. In this case, one of the midpoint capacitor voltages could reach zero volts, while the entire dc-link voltage is applied to the other capacitor, which could carry the breakdown of this capacitor. Additionally, a big offset in the capacitor voltages will prevent a good performance of the system. Consequently, due to the aforementioned reasons, the proposed control for the TPFSC of the SPMSG and for the capacitor voltage offset should have a fast dynamic response.

In this paper, a simple and effective MPCC for the three-phase four-switch machine-side converter (MSC) of the SPMSG oriented for OWC is presented. It is capable of performing highly dynamic speed control and features fixed switching frequency. In this method, only two sector divisions are defined, and three voltage vectors are applied at every switching period without using stator voltage sector location. Moreover, a fast response offset control is introduced in an outer layer of the predictive control without including it in the cost function. The capacitor voltage deviations are quickly suppressed without using any filter. It altogether makes the power plant capable of working properly despite having to deal with large variations of the airflow in the OWC chamber and, as a result, in the incoming energy.

This paper is organized as follows. The proposed MPCC with a fixed switching frequency feature for the TPFSC connected to SPMSG is presented in Section II. In this section, the proposed three voltage vector sequences and the voltage offset control are explained, while the effects of the vectors on the duration times are investigated. In Section III, the OWC-based power plant emulator and the method to extract the maximum energy from the OWC are explained. The simulation results in order to show the dynamic performance of the proposed predictive control and the effectiveness of the voltage offset elimination control are conducted in Section IV. Some of the many experimental tests intended to evaluate the dynamic response and the steadystate performance for an 8.7 kW SPMSG have been included in Section V. Furthermore, the TPFSC and the offset control were tested using an OWC power plant emulator with irregular waves in order to check the performances of the proposed method. Finally, the conclusions of this paper are presented in Section VI.

# II. MODEL PREDICTIVE CURRENT CONTROL FOR THE SPMSG WHEN FED BY TPFSC

The proposed fault-tolerant topology for the MSC of an SPMSG is shown in Fig. 2. In this topology, when a semiconductor open-circuit or short-circuit fault is detected and located, the faulty leg will be isolated and then by trigging the corresponding anti-parallel thyristors or triacs the faulty phase will

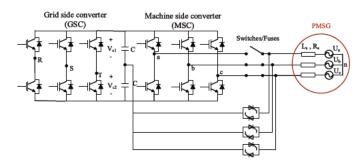


Fig. 2. Fault-tolerant topology schematic for the machine-side converter of a SPMSG.

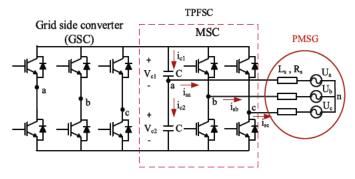


Fig. 3. Structure of a TPFSC connected to a SPMSG.

be connected to the midpoint of the dc bus as shown in Fig. 2. The semiconductor open-circuit and short-circuit faults can be diagnosed by different methods such as modified normalized dc current, current deviation, wavelet, fuzzy, and neural network-based methods, *di/dt* feedback control, and vector composition of output voltage methods [28].

In this paper, it is assumed that the faulty leg, which is phase "a," was detected and isolated and the phase "a" is connected to the midpoint of the dc capacitors. As a result, the structure of the proposed TPFSC for the SPMSG is shown in Fig. 3.

## A. Current Slopes

The dynamic equations for an SPMSG in rotor rotating reference frame can be presented as follows [20]:

$$u_{sd} = R_s i_{sd} + \frac{d}{dt} \lambda_{sd} - \omega_m \lambda_{sq} \tag{1}$$

$$u_{sq} = R_s i_{sq} + \frac{d}{dt} \lambda_{sq} + \omega_m \lambda_{sd} \tag{2}$$

where  $u_s, i_s, \lambda_s, \omega_m$ , and  $R_s$  are the stator voltage, the stator current, the stator flux, the electrical rotor speed, and the stator resistance, respectively. The SPMSG stator flux in the rotor reference frame can be expressed as follows:

$$\lambda_{sd} = L_s i_{sd} + \lambda_f \tag{3}$$

$$\lambda_{sq} = L_s i_{sq} \tag{4}$$

where  $\lambda_f$  and  $L_s$  represent the permanent magnets flux and the stator inductance, respectively. The current slopes in the dq

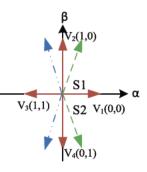


Fig. 4. Voltage vectors positions in the stationary reference frame for a TPFSC. Red vectors:  $v_{c1} = v_{c2}$ ; green vectors:  $v_{c1} < v_{c2}$ ; blue vectors:  $v_{c1} > v_{c2}$ .

frame can be obtained according to (1)-(4) as follows:

$$\frac{d}{dt}i_{sd} = \frac{1}{L_s} \left( -R_s i_{sd} + \omega_m L_s i_{sq} + u_{sd} \right) \tag{5}$$

$$\frac{d}{dt}i_{sq} = \frac{1}{L_s} \left( -R_s i_{sq} - \omega_m L_s i_{sd} - \omega_m \lambda_f + u_{sq} \right).$$
(6)

As a result, the stator current derivatives can be derived according to the machine parameters, the speed, the permanent magnet flux, the currents, and the applied stator voltage.

# B. TPFSC Voltage Vectors

Four possible combinations of the switch states can be taking place in the TPFSC. When the faulty phase is the phase "a," see Fig. 3, the converter voltage in the *abc* frame can be presented as follows:

$$\begin{cases} V_{S_a} = \frac{1}{3} v_{c1} (-S_b - S_c) + \frac{1}{3} v_{c2} (2 - S_b - S_c) \\ V_{S_b} = \frac{1}{3} v_{c1} (2S_b - S_c) + \frac{1}{3} v_{c2} (2S_b - S_c - 1) \\ V_{S_c} = \frac{1}{3} v_{c1} (2S_c - S_b) + \frac{1}{3} v_{c2} (2S_c - S_b - 1) \end{cases}$$
(7)

where  $V_{sa}$ ,  $V_{sb}$ , and  $V_{sc}$  are the converter voltages in the *abc* frame,  $v_{c1}$  and  $v_{c2}$  are the upper and lower capacitor voltages and  $S_b$  and  $S_c$  are the switching states of the phase b and c, respectively, which can be 1 or 0. The converter voltage in the *abc* frame can be transferred to the stationary alpha–beta frame according to the Clarke transformation which can be presented as follows:

$$\begin{bmatrix} v_{S\alpha} \\ v_{S\beta} \end{bmatrix} = \begin{bmatrix} 1 & -\frac{1}{2} & -\frac{1}{2} \\ 0 & \frac{\sqrt{3}}{2} & -\frac{\sqrt{3}}{2} \end{bmatrix} \begin{bmatrix} v_{Sa} \\ v_{Sb} \\ v_{Sc} \end{bmatrix}.$$
 (8)

As a result, the converter voltage vectors in the alpha-beta frame can be obtained from (7) and (8). The four possible voltage vectors when the faulty phase is "a," are shown in Fig. 4, and the corresponding values for these four vectors in the stationary and *abc* frames are shown in Table I. As can be seen in Table I, the voltage values of  $V_2$  and  $V_4$  in the  $\alpha$ -axis depend on the differences between the upper and lower capacitor voltages. Hence, these two vector phases could change as shown in Fig. 4. For example, when the upper capacitor has a bigger voltage than the lower capacitor,  $V_2$  and  $V_4$  will be positioned like the blue lines shown in Fig. 4.

TABLE I FOUR VOLTAGE VECTOR VALUES FOR THE TPFSC IN THE " $\alpha-\beta$  " Stationary and "abc" Frame

S <sub>b</sub>	$S_{c}$	Vector	$V_{Sa}$	$V_{Sb}$	$V_{Sc}$	$V_{S\alpha}$	$V_{S\beta}$
	0		2v <sub>c2</sub> / 3	$-v_{c2}/3$	-v <sub>c2</sub> / 3	2v <sub>c2</sub> / 3	0
1	0	$V_2$	$(v_{c2} - v_{c1})/3$	$(2v_{c1} + v_{c2})/3$	$-(v_{c1}+2v_{c2})/3$	$(v_{c2} - v_{c1})/3$	$\sqrt{3}(v_{c1}+v_{c2})/3$
1	1	$V_3$	$-2v_{c1}/3$	v <sub>c1</sub> / 3	<i>v</i> <sub>c1</sub> / 3	$-2v_{c1}/3$	0
0	1	$V_4$	$(v_{c2} - v_{c1})/3$	$-(v_{c1}+2v_{c2})/3$	$(2v_{c1} + v_{c2})/3$	$(v_{c2} - v_{c1})/3$	$-\sqrt{3}(v_{c1}+v_{c2})/3$

TABLE II VOLTAGE VECTOR SEQUENCES ACCORDING TO THE STATOR VOLTAGE SECTOR

Vector sequence for sector 1:	V <sub>1</sub>	$V_2$	V <sub>3</sub>
Vector sequence for sector 2:	Vı	$V_4$	$V_3$

The stator current slopes for these four voltage vectors can be calculated according to the corresponding voltage values using (5) and (6).

# C. Voltage Vector Sequences and Sector Division Definition

Three vectors among the four possible voltage vectors are employed in every switching period according to the stator voltage sector, to have fixed switching frequency and low current ripple. In this strategy, two stator voltage sectors are defined, as can be seen in Fig. 4. If the equivalent stator voltage value in the  $\beta$ -axis is positive, the stator sector is one ( $S_1$ ), otherwise, the sector is two ( $S_2$ ). The proposed voltage vector sequences according to the corresponding sector are shown in Table II. As can be seen, in each switching period,  $V_1$  and  $V_3$  will be applied. A part of  $V_1$  or  $V_3$  and the whole opposite vector will produce the equivalent zero vector.

The switching pattern of the proposed voltage vector sequences when the equivalent stator voltage is in sector 2 ( $S_2$ ) is shown in Fig. 5. Three voltage vectors are symmetrically employed in each period. As a result, the stator current at the end of period (k+1) can be presented as follows:

$$i_{sq}(k+1) = i_{sq}(k) + S_{qa}t_a + S_{qb}t_b + S_{qc}t_c$$
(9)

$$i_{sd}(k+1) = i_{sd}(k) + S_{da}t_a + S_{db}t_b + S_{dc}t_c$$
(10)

where  $S_{qa}$ ,  $S_{qb}$ ,  $S_{qc}$  and  $S_{da}$ ,  $S_{db}$ ,  $S_{dc}$  are the current derivatives of the corresponding voltage vectors in the q- and d-axis,

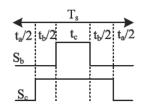


Fig. 5. Proposed MPCC switching pattern for the TPFSC when the stator voltage is in  $S_2$ .

respectively. The duration times of the first, the second, and the third vector are called  $t_a$ ,  $t_b$ , and  $t_c$ , respectively.

The error between the measured current and its reference value at the end of a switching period can be minimized by the following cost function:

$$G(k+1) = (i_{sq}(k+1) - i^*_{sq}(k))^2 + (i_{sd}(k+1) - i^*_{sd}(k))^2$$
(11)

where the superscript \* represents the reference values. Substituting (9) and (10) into (11) and considering that the sum of the duration times of the voltage vectors are equal to the switching period time, leads to

$$G(k+1) = (i_{sq}(k) + S_{qa}t_a + S_{qb}t_b + S_{qc}(T_s - t_a - t_b) - i_{sq}^*(k))^2 + (i_{sd}(k) + S_{da}t_a + S_{db}t_b + S_{dc}(T_s - t_a - t_b) - i_{sd}^*(k))^2.$$
(12)

The optimized duration times which minimize the cost function can be achieved by setting to zero the partial derivatives of (12) with respect to  $t_a$  and  $t_b$ . Consequently, the optimized duration time of these three vectors can be obtained as follows: eq. 13 and 14 shown at the bottom of this page,

$$t_c = T_{\rm sw} - t_a - t_b \tag{15}$$

nd *d*-axis, where  $T_{sw}$  is the switching period time.

$$t_{a} = \frac{\left(i_{sq}(k) - i_{sq}^{*}(k)\right) \cdot \left(S_{db} - S_{dc}\right) + \left(i_{sd}(k) - i_{sd}^{*}(k)\right) \cdot \left(S_{qc} - S_{qb}\right)}{S_{da}(S_{qb} - S_{qc}) + S_{db}(S_{qc} - S_{qa}) + S_{dc}(S_{qa} - S_{qb})} + \frac{T_{sw}\left(S_{db} \cdot S_{qc} - S_{dc} \cdot S_{qb}\right)}{S_{da}(S_{qb} - S_{qc}) + S_{db}(S_{qc} - S_{qa}) + S_{dc}(S_{qa} - S_{qb})}$$
(13)

$$t_{b} = \frac{\left(i_{sq}(k) - i_{sq}^{*}(k)\right) \cdot \left(S_{dc} - S_{da}\right) + \left(i_{sd}(k) - i_{sd}^{*}(k)\right) \cdot \left(S_{qa} - S_{qc}\right)}{S_{da}(S_{qb} - S_{qc}) + S_{db}(S_{qc} - S_{qa}) + S_{dc}(S_{qa} - S_{qb})} + \frac{T_{sw}\left(S_{dc} \cdot S_{qa} - S_{da} \cdot S_{qc}\right)}{S_{da}\left(S_{qb} - S_{qc}\right) + S_{db}\left(S_{qc} - S_{qa}\right) + S_{dc}\left(S_{qa} - S_{qb}\right)} \tag{14}$$

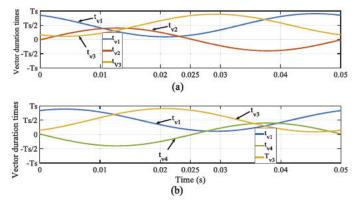


Fig. 6. Duration times of the voltage vector sequences at steady state for: (a)  $V_1$ ,  $V_2$  and  $V_3$ ; (b)  $V_1$ ,  $V_4$  and  $V_3$ .

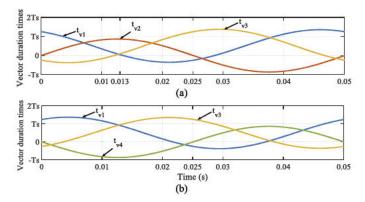


Fig. 7. Duration times of the voltage vector sequences in a transient state for: (a)  $V_1$ ,  $V_2$  and  $V_3$ ; (b)  $V_1$ ,  $V_4$  and  $V_3$ .

# D. Identification Algorithm for the Stator Voltage Sector and Correction of the Duration Times

Fig. 6(a) and (b) show the duration times of the two possible voltage vector sequences during one rotational cycle of the rotor in steady state when the speed is 400 r/min. As can be seen in Fig. 6(a), the duration times of the voltage vector sequence of sector 1, duration times of  $(V_1 - V_2 - V_3)$ , are all positive from the beginning until t = 0.025 s. However, from t = 0.025 s to the end of the cycle, the duration time for  $V_2$  has a negative value. In contrast, according to Fig. 6(b), for the other voltage vector sequence  $(V_1 - V_4 - V_3)$ , from the beginning of the cycle to t =0.025 s, the duration time of  $V_4$  has a negative value and from t = 0.025 s to the end of the cycle, the duration times of all vectors of this vector sequence are positive values. This case shows that, from t = 0 s to t = 0.025 s and from t = 0.025 s to t = 0.05 s, the stator voltage reference is located in sector 1 and sector 2, respectively. Consequently, when the duration time of the second vector  $(t_b)$  has a negative value in the selected vector sequence, it indicates that the correct vector sequence is the other vector sequence.

Another case can arise during transients or when the required stator voltage is higher than that the converter can generate, since in this situation there is no need to generate a zero-voltage vector by the combination of the two opposite vectors  $V_1$  and  $V_3$ . Hence, the duration time of  $V_1$  or  $V_3$  ( $t_a$  or  $t_c$ ) could also become negative as Fig. 7(a) and (b) show.

As a result, the vector that has a negative duration time should be removed from the voltage vector sequence and the other two voltage vectors should be normalized and applied by the MSC. For example, if the required voltage is high, and it is located at the left side of sector 1, the correct vectors are  $V_2$  and  $V_3$  and there is no need to apply  $V_1$ . For instance, as it is shown in Fig. 7(a), from t = 0 s to t = 0.013 s the duration times of  $V_1$  and  $V_2$  are both positive, whereas the time for  $V_3$  is negative. This fact shows that the required stator voltage is located at the right side of sector 1, and there is no need to apply  $V_3$ . Another case is from t = 0.013 s to t = 0.025 s when the duration time of  $V_1$  is negative and the duration times of  $V_2$  and  $V_3$  are positive. In this case, there is no need to apply  $V_1$ . As a result, when the estimated duration time of the first or the third voltage vector ( $t_a$ or  $t_c$ ) has a negative value, the corresponding vector is removed, and the two other vectors should be normalized, which means

$$\text{if } t_c \leq 0 \Rightarrow \begin{cases} t'_a = \frac{t_a}{t_a + t_b} T_s \\ t'_b = \frac{t_b}{t_a + t_b} T_s \\ t'_c = 0 \end{cases}$$

$$\text{if } t_a \leq 0 \Rightarrow \begin{cases} t'_a = 0 \\ t'_b = \frac{t_b}{t_b + t_c} T_s \\ t'_c = \frac{t_c}{t_b + t_c} T_s \end{cases}$$

$$(16)$$

where  $t'_a$ ,  $t'_b$ , and  $t'_c$  are the normalized values of the  $t_a$ ,  $t_b$ , and  $t_c$ , respectively. These duration times of the vectors are applied when the duration time of the first or the third vector in the vector sequence gets negative. In this case, the output voltage will be limited to the maximum voltage of the converter without overmodulation.

As aforementioned, when the duration time of second vector  $(t_b)$  is negative, the correct vector sequence is the other one. This case also can be seen in Fig. 7(a) and (b). As can be seen in this figure, when the duration time of  $V_2$  is negative, the duration time of  $V_4$  is positive and vice versa.

In summary, when the duration time of the second vector  $(t_b)$  has a negative value during any situation, transient or steady state, the correct sector is not the one chosen but the other one, and it is necessary to calculate the duration times of the other vector sequence. Moreover, if the duration time of the first or third vector becomes negative, the corresponding vector should be removed, and the duration time of the other two vectors should be normalized according to (16).

# E. Voltages Offset Elimination Control

A voltage offset can appear between the upper and the lower capacitors of the dc link due to the ac current flowing through the capacitors. This voltage deviation cause to have unbalanced three phase currents.

According to Fig. 3, the stator current that circulates through the capacitors, in phase "a," can be expressed by the two capacitors voltages as follows:

$$i_{sa} = C \frac{dv_{c1}}{dt} - C \frac{dv_{c2}}{dt}$$
(17)

where C is the capacitors value in the dc link and  $V_{c1}$  and  $V_{c2}$  are upper and lower capacitor voltages, respectively. The voltage differences between the upper and the lower capacitors can be derived as follows:

$$v_{c1}(t) - v_{c2}(t) = \frac{1}{C} \int_0^t i_{sa} dt + v_{c1}(0) - v_{c2}(0).$$
(18)

As a result, the voltage difference is related to the initial value of the capacitors voltage and the faulty phase current. The threephase stator currents will be unbalanced when a voltage offset exists in the capacitors. In this case, the stator currents can be expressed by

$$i_{sa} = I_s \cos(\omega_m t) - \bar{i}_{so} \tag{19}$$

$$i_{sb} = I_s \cos\left(\omega_m t - \frac{2\pi}{3}\right) + \frac{1}{2}\bar{i}_{so} \tag{20}$$

$$i_{sc} = I_s \cos\left(\omega_m t + \frac{2\pi}{3}\right) + \frac{1}{2}\bar{i}_{so} \tag{21}$$

where  $i_{sa}$ ,  $i_{sb}$ , and  $i_{sc}$  are the three-phase stator currents,  $I_s$  is the amplitude of the ac current and  $\overline{i}_{so}$  is the dc component current. By transferring the three-phase stator currents to the stationary frame, according to (19)–(21), the current yields to

$$i_{s\alpha} = I_s \cos(\omega_m t + \alpha) - i_{so} \tag{22}$$

$$i_{s\beta} = I_s \sin(\omega_m t + \alpha). \tag{23}$$

As can be seen, the dc component of the current will only be in  $\alpha$  axis. According to (18) and (19), the capacitor voltage differences can be represented as follows:

$$v_{c1}(t) - v_{c2}(t) = \frac{1}{C\omega_m} I_s \sin(\omega_m t + \alpha) - \frac{\overline{i}_{so}}{C} t - \frac{1}{C\omega_m} I_s \sin(\alpha) + v_{c1}(0) - v_{c2}(0).$$
(24)

In (24), only the first term is an ac term in the right side of the equation and the rest of the terms are the dc terms that should have a zero value in order to eliminate the offset. Therefore, the  $i_{so}$  needs to be regulated to remove the voltage offset.

If  $\frac{i_{s\beta}}{C\omega_m}$  is subtracted from both sides in (24), and (23) is substituted in the right side of (24), the result yields to

$$v_{c1}(t) - v_{c2}(t) - \frac{i_{s\beta}}{C\omega_m} = -\frac{1}{C\omega_m} I_s \sin(\alpha) - \frac{i_{so}}{C} t + v_{c1}(0) - v_{c2}(0).$$
(25)

This equation indicates that by subtracting  $\frac{i_{s\beta}}{C\omega_m}$  from the capacitor voltage difference, the dc voltage offset can be obtained. Therefore, if the right-hand side of (25) is kept equal to zero, the dc voltage offset will be eliminated. As can be seen in (25), this goal can be achieved by only controlling the  $i_{so}$  current. According to (22) and (23), this dc current only exists in the  $\alpha$ -axis

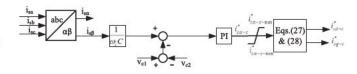


Fig. 8. Block diagram of the proposed voltage deviation control.

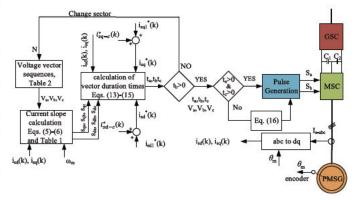


Fig. 9. Block diagram of the proposed MPCC for TPFSC fed PMSG.

component of the stator current. Consequently, by regulating the left-hand side of (25) with a PI controller, the compensation current for removing the voltage offset in the  $\alpha$ -axis can be presented as follows:

$$i_{s\alpha-c}^* = -k_p \left( v_{c1}(t) - v_{c2}(t) - \frac{i_{s\beta}}{C\omega_m} \right)$$
$$-k_I \int \left( v_{c1}(t) - v_{c2}(t) - \frac{i_{s\beta}}{C\omega_m} \right) dt.$$
(26)

This compensation current can be transferred to the synchronous frame as follows:

$$i_{sd-c}^* = i_{s\alpha-c}^* \cos(\theta_m) \tag{27}$$

$$_{sq-c}^{*} = -i_{s\alpha-c}^{*}\sin(\theta_m). \tag{28}$$

Therefore, the compensation current reference in the dq frame for eliminating the offset voltage can be obtained from (27) and (28). The block diagram of the proposed voltage offset elimination control is presented in Fig. 8.

When the voltage offset elimination control is working, the upper and lower capacitor voltages can be expressed by

$$v_{c1}(t) = -v_{c2}(t) = \frac{1}{2} \frac{1}{C\omega_m} I_s \sin(\omega_m t + \alpha) + \frac{V_{dc}}{2}.$$
 (29)

The ac term in the capacitor voltage is dependent on the machine speed, the stator current, and the capacitance of the capacitor. So, some type of over voltage protection must be provided as a preventive measure, mainly at low speeds.

# F. Overall Proposed Model Predictive Direct Current Control With Voltage Suppression Control for TPFSC Fed SPMSG

The block diagram of the proposed MPCC with voltage suppression control for the TPFSC fed PMSG is shown in Fig. 9. As can be seen, the current references in the synchronous frame consist of two terms, one is the compensatory term to suppress the voltage offset in the capacitors, which is obtained according to Fig. 8, and the other term is the reference value which should be chosen in a way that the maximum power could be obtained from the OWC.

In each period of this control strategy, three voltage vectors are selected according to Table II. It is assumed that the vector sequence is the same as those in the previous period. The current slopes in the synchronous frame are calculated according to (5) and (6) using the rotor speed and the stator current for these three voltage vectors. The duration times of this vector sequence are obtained according to the estimated current slopes and the current references in the dq frame, according to (13)–(15). If these estimated times are positive, this vector sequence and their corresponding times will be applied by the converter. But, if the second vector duration time obtains a negative value, the vector sequence should be changed to the other vector sequence, and the corresponding new durations should be calculated, which led to have a positive duration for the second voltage vector in this new sequence. After this step, as it is shown in Fig. 9, the first and third vector times will be analyzed to check if they are positive or negative. If one of these vectors is negative, the duration times of these vectors are modified according to (16). Finally, these vectors and their durations are applied to the PMSG by the MSC.

# III. OWC-BASED POWER PLANT EMULATOR

The performance of the proposed MPCC for TPFSC has been tested in a demanding wave energy application. In order to reproduce a wave energy power plant in the laboratory, it was necessary to build an experimental emulator reproducing the components and the behavior of an OWC-based power plant [14], [29]. The control strategy chosen in this paper is to keep the power plant working at the maximum efficiency point by controlling the rotor speed. This section describes how the OWC is modeled in this paper.

## A. OWC Emulation

The OWC is reproduced using the following mathematical models.

1) Irregular Wave Model: Real waves, also called irregular waves, can be reproduced using a variety of mathematical models that represent the state of the free surface of the sea by its energy spectrum,  $S(\omega)$ . The most known spectra are Pierson–Moskowitz [30]–[33], JONSWAP [34]–[37], TMA [30], [38], and Bretschneider [39]. All of them reproduce the sea state keeping the same spectral characteristics (significant height, peak period, etc.) as the original real waves used to obtain the  $S(\omega)$ .

The Pierson–Moskowitz spectrum was the one chosen in this paper

$$S(\omega) = \frac{5}{16} \cdot H_s^2 \cdot \omega_p^4 \cdot \omega^{-5} \exp\left[-\frac{5}{4} \cdot \left(\frac{\omega}{\omega_p}\right)^{-4}\right]$$
(30)

where  $\omega = 2\pi/T$  is the angular frequency of the wave,  $H_S$  is the significant wave height, and  $\omega_p = 2\pi/T_p$  is the angular spectral peak frequency.

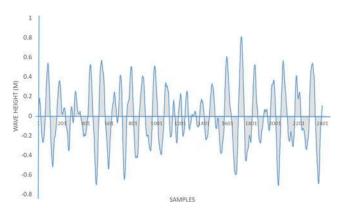


Fig. 10. Real wave profile used in this paper comprises 2400 samples (each sample is 0.1 seconds).

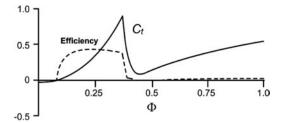


Fig. 11. Ct and efficiency as a function of the flow coefficient.

An irregular wave is represented by the addition of *n* regular waves, according to the equation

$$\eta(x,t) = \sum_{i=1}^{n} \zeta_i \cdot \cos(\omega_i t - k_i x + \varepsilon_i)$$
(31)

where  $\zeta_i$  is the amplitude of each harmonic, which value is calculated from the Pierson–Moskowitz energy spectrum

$$\zeta_i = \sqrt{2 \cdot S\left(\omega_i\right) \cdot \Delta\omega_i}.$$
(32)

Each regular wave is obtained with a random phase with the same probability of occurrence, expressed by a constant probability density in the interval  $(-\pi, \pi)$ . Fig. 10 shows the real wave profile  $(H_s = 1 \text{ and } T_s = 10)$ , where  $T_S$  is the significant wave period) used in the tests carried out in this paper.

2) Chamber Model: The chamber model of an OWC involves a number of complex equations [40]–[42]. However, the model implemented in the emulator uses a set of simplified equations and, as a consequence, the hydrodynamics inside the chamber are not fully described. Nevertheless, from the electrical point of view, the electrical power injected into the grid does not lose the multifrequency spectrum of the wave resource.

The air flow through the chamber presents a speed that is calculated (see Tables IV and V in the Appendix for the nomenclature)

$$V_x = \left(\frac{A_c}{A_d}\right) \cdot \frac{\partial h_c}{\partial t}.$$
(33)

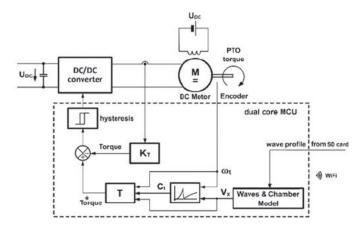


Fig. 12. Control block of the OWC and Wells turbine emulator.

## B. Wells Turbine Torque

The torque developed by the Wells turbine depends on the flow coefficient through the turbine,  $\Phi$ , Fig. 11 in solid line

$$\Phi = \frac{V_x}{r_t \cdot \omega_t}.$$
(34)

For a given wave, a dual-core microcontroller, MCU-1 calculates the torque coefficient,  $C_t$  and obtains the Wells turbine torque

$$T_t = C_t \cdot K \cdot \text{Radius} \cdot \left[ V_x^2 + (\text{Radius} \cdot \omega_t)^2 \right].$$
(35)

This torque value is reproduced in the test bench using the current controlled separately excited dc motor showed in Fig. 12.

# C. PTO Control

The system described in Section III-A and B generates the same torque as a Wells turbine would apply to the generator shaft, but the corresponding rotary speed depends on the load torque that faces. In this part, another dual-core microcontroller, MCU-2, controls the SPMSG to generate the opposite torque that makes the emulated Wells turbine to work at the maximum efficiency point in every moment. The efficiency curve of the turbine, see Fig. 11, dashed line, provides the value of the  $\Phi$  for the maximum efficiency. From this value and calculating  $V_x$  in every control cycle, MCU-2 obtains the rotary speed,  $\omega_t$ , for the maximum efficiency and uses it as the reference speed in the SPMSG control system.

# IV. SIMULATION RESULTS

The simulation studies for a TPFSC feeding an 8.7 kW SPMSG is carried out in MATLAB/Simulink environment to validate the proposed MPCC. The machine parameters and capacitor values for the simulation studies as well as the experimental tests are illustrated in Table III. The switching frequency and the sampling period are 4 kHz and 250  $\mu$ s, respectively. The grid side converter was controlled to maintain the dc-link voltage at 550 V.

TABLE III SPMSG PARAMETERS

DC Link voltage	550 V	Permanent magnet Flux	1.05 Wb
Rated power	8.7 kW	Stator Inductance	50 mH
Rated voltage	400 V	Stator resistance	2 Ω
Pole pairs	3	C <sub>1</sub> , C <sub>2</sub>	1080 μF

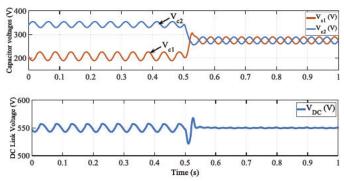


Fig. 13. Simulation results of capacitor voltages and dc-link voltage with and without the offset control.

First, the voltage offset control is tested and the results are shown in Fig. 13. In this study, the  $i_{sd}$  and  $i_{sq}$  references were set to 0 and -4 A, respectively. The rotor speed was fixed at 400 r/min for this case. At the beginning, the voltage offset control was disabled. However, at t = 0.5 s, the compensation currents for removing the offsets were employed. As it can be seen in Fig. 13, the offset voltage between the two capacitors before t = 0.5 s is about 140 V. Additionally, the total dc-link voltage has oscillations around the 550 V value when the dc voltage deviation control is disabled. However, when the offset control is enabled, both capacitors mean voltages are set to 275 V and the total dc-link voltage oscillation is removed.

The stator currents in the dq and abc frame, the sector and duration times of the vectors without and with the compensation control are shown in Figs. 14 and 15, respectively. Clearly, the currents in the dq frame could not follow the reference values in some points when the offset control is not working, which led to have a distorted and unbalanced current in phase a. This issue takes place since the upper capacitor  $(v_{C1})$  has a lower voltage when there is not an offset control, meaning that the voltage vector  $V_3$  has a lower value compared to normal situation. Therefore, when the reference voltage is at the end of sector 1 and at the beginning of sector 2, even by employing  $V_3$  for the whole switching period, the required voltage will not be produced. Consequently, at the end of sector 1 and at the beginning of sector 2, the currents cannot follow the references when the offset control is not working. On the contrary, the currents are following the reference values when the proposed compensation control is working and, as a result, the currents are sinusoidal and balanced.

To verify the dynamic response, the generator speed was decreased linearly from 500 to 200 r/min during 1.5 s, as shown in Fig. 16. Furthermore, the  $i_{sq}$  and  $i_{sd}$  references were changed

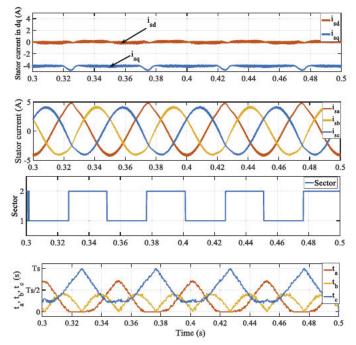


Fig. 14. Simulation results without capacitor voltage offset elimination control.

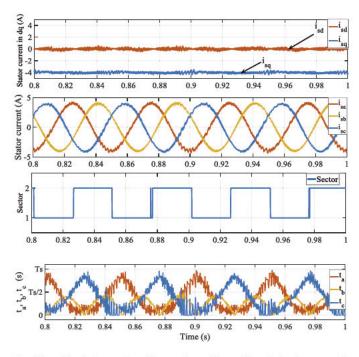


Fig. 15. Simulation results with capacitor voltage offset elimination control.

from -6 to -3 A at t = 0.7 s and from -1 to 1 A at t = 1 s, respectively. The results are depicted in Fig. 16. In this test, the compensation control was applied for the whole test. The results indicate that the currents follow the reference values rapidly during different rotor speeds, while the capacitor voltage offset remained at zero for the whole test. Moreover, the three-phase currents are balanced due to the offset control.

The proposed model predictive control for TPFSC is compared with two previous predictive controls, one voltage vector [43] and two voltage vector predictive controls [44], for this

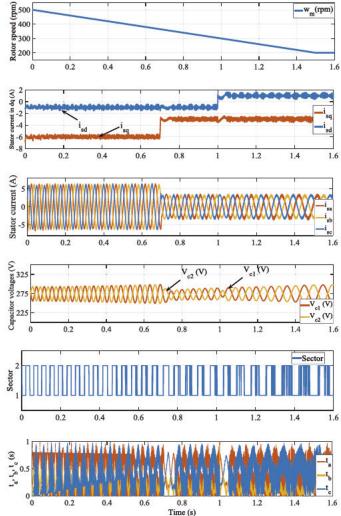


Fig. 16. Simulation results during a rotor speed variation with current step changes.

same converter topology at the same switching frequency. In this test, the rotor speed was 400 r/min and the current references for d and q are set to 0 and -6 A, respectively. The three-phase stator currents and current total harmonic distortion (THD) for this comparison are shown in Figs. 17 and 18, respectively. The results show that the stator current for the proposed method is sinusoidal, balanced, and free of ripples when compared to the two previous predictive strategies. The stator current THD for the one voltage vector predictive control, two voltage vector predictive control, and the proposed strategy are 10.22%, 5.96%, and 3.47%, respectively. Moreover, the low-order harmonics are decreased with the proposed control method while for the two previous control methods, these harmonics are notable. It should be noted that the current THD for three-phase six switches converter with the same reference values would be 1.32% in this case (pre-fault situation).

## V. EXPERIMENTAL RESULTS

The proposed MPCC of a TPFSC for SPMSG was tested in a wave energy application using the experimental setup shown in Figs. 19 and 20. This setup consists of: Part 1: OWC Power

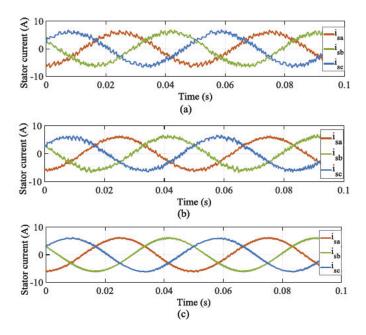


Fig. 17. Three-phase stator current. (a) One voltage vector predictive control. (b) Two voltage vector predictive control. (c) Proposed model predictive control.

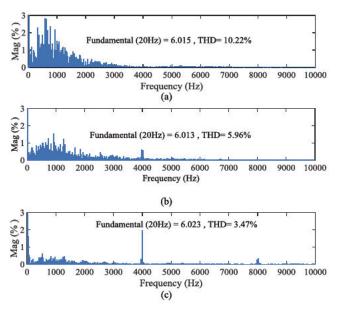


Fig. 18. Stator current THD. (a) One voltage vector predictive control. (b) Two voltage vector predictive control. (c) Proposed model predictive control.

Plant, which reproduces the waves, OWC models, and actual torque developed by a Wells turbine emulated by means of a dc–dc converter and a dc machine; Part 2: Power Take Off, where a TPFSC is connected to the SPMSG and is controlled with the proposed MPCC; and Part 3: Grid connection, which injects into the grid the energy that has been absorbed from the waves. Each of these three parts is controlled using a dual-core F28M35x Concerto MCU of Texas Instruments. The switching frequency and the sampling time for the MSC were selected to be 4 kHz and 250  $\mu$ s. All of the waveforms were recorded using the F28M35x itself, except the stator currents, that were recorded using a Tektronix TDS2024C oscilloscope with a sampling time of 100  $\mu$ s. The dc-link voltage was set to 550 V during the

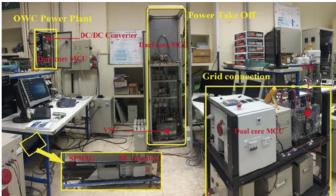


Fig. 19. Photo of the experimental setup used to reproduce an OWC-based power plant.

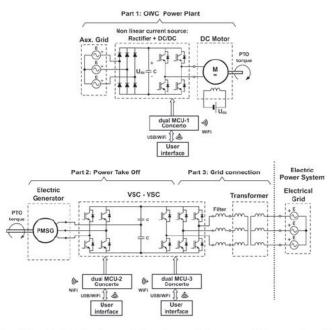


Fig. 20. Electrical scheme of the setup used to carry out the experimental tests.

experimental tests. The machine parameters and the capacitor values are illustrated in Table III. The capacitor values for the TPFSC are the same capacitor values that are used in the threephase six switches converters.

Three types of experimental tests were carried out: first, a steady-state test to check the proper operation of the control system. Second, three transient tests to check the performance of the control system during changes of the  $i_{sd}$  and  $i_{sq}$  references and when facing a rotor speed step. Finally, a transient test reproducing the behavior of an OWC wave energy power plant. In all of these tests, the voltage offset elimination control was enabled.

# A. Steady-State Test

In this first test, the rotor speed was set to 450 r/min, as can be seen in Fig. 21, by means of the dc motor, whereas the values for the  $i_{sd}$  and  $i_{sq}$  references were set to 0 and -4 A, respectively. Fig. 22 shows the currents in the d-q frame. As

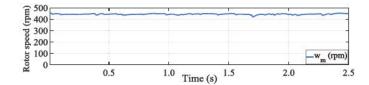


Fig. 21. Rotor speed during the steady-state test.

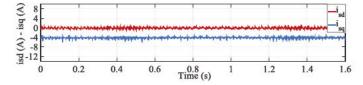


Fig. 22. Values of  $i_{sd}$  (red) and  $i_{sq}$  (blue) obtained from the phase current measurements.

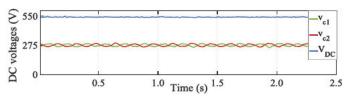


Fig. 23. DC voltages in C1 (light green) and C2 (red) and DC-link voltage (blue) during the steady-state test.

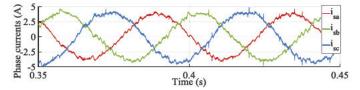


Fig. 24. Three phase stator current of the SPMSG during the steady-state test.

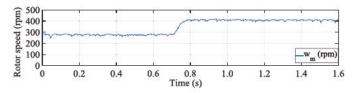


Fig. 25. SPMSG rotor speed during the speed transient test.

can be checked, the currents are following the reference values perfectly. The dc-link voltage and the capacitor voltages are shown in Fig. 23. As can be noticed in this figure, the dc-link voltage is smooth without practically any ripple. Moreover, the capacitor voltages are balanced, and the voltage mean value for each of the capacitors is 275 V, which shows the effectiveness of the proposed voltage offset elimination control. Fig. 24 shows the three-phase current proving that the proposed control system was able to obtain sinusoidal currents, despite the lack of two IGBTs with respect to the standard two-level converter topology.

## **B.** Speed Transient Test

This test was meant to check the capability of the proposed control to maintain the  $i_{sd}$  and  $i_{sq}$  currents, and consequently, the torque and magnetic field of the SPMSG, close to their references when the rotary speed faces a step change from 270 to 400 r/min, as shown in Fig. 25. The test was carried out by

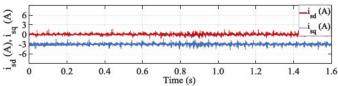


Fig. 26.  $i_{sd}$  (red) and  $i_{sq}$  (blue) currents during the speed transient test.

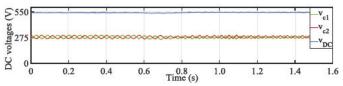


Fig. 27. DC voltages in C1 (light green) and C2 (red) and dc-link voltage (blue) during the speed transient test.

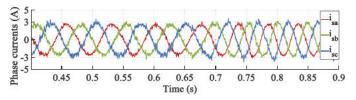


Fig. 28. Phase currents at the stator of the PMSM during the speed transient test.

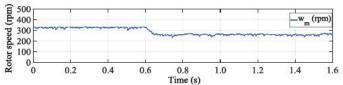


Fig. 29. SPMSG rotor speed during the  $i_{sq}$  transient test.

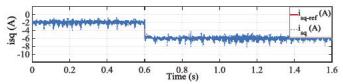


Fig. 30.  $i_{sq}$  reference (red) and actual  $i_{sq}$  (blue) during the  $i_{sq}$  transient test.

applying a dc voltage-step to the armature winding of the dc motor. Fig. 26 shows that the  $i_{sd}$  and  $i_{sq}$  remained constant despite the speed step. Fig. 27 shows the capacitor voltages during this test. The two capacitor voltages and the total dc-link voltage remained stable when the rotor speeds up during this test. Finally, Fig. 28 shows the three-phase stator current during the speed step that took place in the SPMSG. It can be checked clearly that the frequency of the stator current is increased after the step.

# C. isq Transient Test

In this test, the SPMSG torque was increased through the  $i_{sq}$  reference from -2 to -6 A, whereas the dc motor supply remained unchanged. As a result, the rotor speed decreased as the load torque generated by the SPMSG temporarily exceeded the motor torque provided by the dc motor. The rotor speed is shown in Fig. 29. Figs. 30 and 31 show the fast dynamic

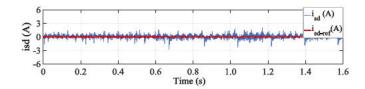


Fig. 31.  $i_{sd}$  reference (red) and actual  $i_{sd}$  (blue) during the  $i_{sq}$  transient test.

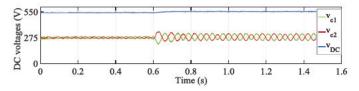


Fig. 32. DC voltages in C1 (light green) and C2 (red) and DC-link voltage (blue) during the  $i_{sq}$  transient test.

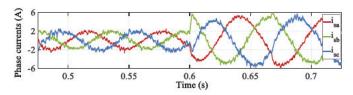


Fig. 33. Phase currents at the stator of the SPMSG around the point where the  $i_{sq}$  transient takes place.

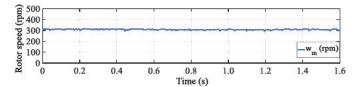


Fig. 34. Rotor speed during the  $i_{sd}$  transient test.

response of the proposed control. As can be seen, the  $i_{sq}$  follows the step in the reference with great accuracy and  $i_{sd}$  remains to zero at every moment. The capacitor voltages are shown in Fig. 32. The dc voltages in C1 and C2 experimented a higher fluctuation after the transient due to the higher current demand and the lower speed. As can be checked in this figure, the voltage offset control works perfectly and quickly during the transient test and both capacitors mean voltage value is maintained at 275 V. Finally, Fig. 33 shows the sinusoidal currents near the  $i_{sq}$  transient test, where the waveforms get adapted to the new amplitude immediately, keeping a low distortion.

## D. i<sub>sd</sub> Transient Test

In this test, the SPMSG changes from a field weakening operation point to an overexcited point ( $i_{sd} = -1$  A to  $i_{sd} = +2$  A), maintaining the torque constant through  $i_{sq} = -4$  A. The field weakening was limited to -1 A on purpose to avoid the irreversible demagnetization of the magnets.

The results are shown in Figs. 34–38. The rotor speed is fixed at 300 r/min as can be seen in Fig. 34. The  $i_{sd}$  and  $i_{sq}$  currents for this test are shown in Figs. 35 and 36, respectively. The results show that the  $i_{sd}$  follows the reference value perfectly in the proposed MPCC, while the  $i_{sq}$  is kept unchanged. The three-phase stator current is illustrated in Fig. 37 which shows

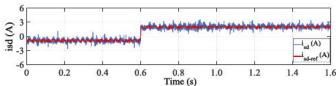


Fig. 35. *isd* reference (red) and actual *isd* (blue) during the *isd* transient test.

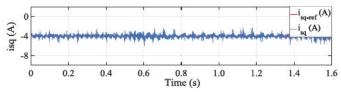


Fig. 36.  $i_{sq}$  reference (red) and actual  $i_{sq}$  (blue) during the  $i_{sd}$  transient test.

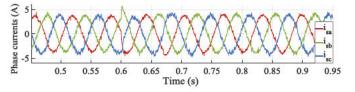


Fig. 37. Phase currents at the stator of the SPMSG around the point where the transient takes place.

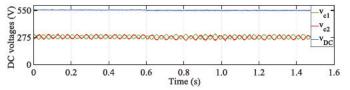


Fig. 38. DC voltages in C1 (light green) and C2 (red) and dc-link voltage (blue) during the  $i_{sd}$  transient test.

that the good quality of the sinusoidal current continued after a brief transient. Fig. 38 shows that the voltage offset control is working properly during the transient while keeping the mean value of the capacitor voltages at the set point value.

#### E. Tests Using the OWC Emulator

The final test was intended to study the proposed control system to be used in an OWC-based power plant. The lack of two IGBTs could mean a reduction of the control capability of the electronic converter, forcing to disconnect it from the electrical grid, leading to a reduction of profits.

The test was carried out for the wave profile showed in Fig. 10 and lasted 4 min. The six microcontrollers that comprise the emulator take care of calculating the air speed in the duct of the OWC, the Wells turbine torque, the Wells turbine maximum efficiency speed to be imposed by the PTO, etc., throughout the entire test, at every control cycle.

In Fig. 39, the Wells turbine maximum efficiency speed and the actual speed are shown in blue and red lines, respectively, throughout the 240 s of the test. This figure shows that the rotary speed of the generator follows the reference speed with great accuracy with the proposed MPCC during the 4 min that lasted the test, in the entire range of speed containing fast and slow

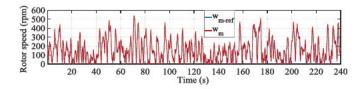


Fig. 39. Speed reference (blue) and actual speed (red) of the SPMSG along the 4 min test.

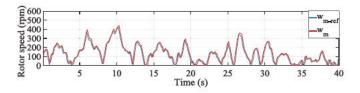


Fig. 40. Detailed view of the speed reference (blue) and actual speed (red) of the SPMSG along the first 40 s of the test.

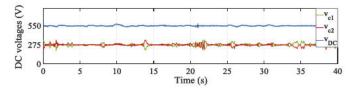


Fig. 41. DC voltages in C1 (light green) and C2 (red) and dc-link voltage (blue) during the test.

reference speed changes. Fig. 40 provides a more detailed view throughout the first 40 s of the test.

Another critical issue is to check the performance of the voltage offset elimination control during this test since, if it is not fast and accurate enough, it could lead to the breakdown of the capacitors. Fig. 41 shows the capacitor voltages and the dc-link voltage of the first 40 s of the test. It can be seen that the proposed method was capable of eliminating the voltage offset of the capacitors during the huge variations of speed, which indicates the effectiveness of the proposed MPCC and the offset elimination control.

## VI. CONCLUSION

This paper presented a simple model predictive current control with capacitor voltage offset elimination control for the TPFSC feeding a SPMSG. The proposed method features a fixed switching frequency and a fast dynamic response despite the fact that it is working with only four switches. In every switching period, three voltage vectors are employed according to the current references and the rotor speed. The proposed capacitor voltage suppression control removes the voltage offset in the capacitors rapidly without needing to obtain the mean values of the capacitor voltages using a low-pass filter which would be difficult at low speeds.

Despite the lack of two IGBTs in the power take off system, the proposed MPCC system has proven to be able to set and keep the references, basically the torque and magnetic field controlled by  $i_{sq}$  and  $i_{sd}$  as well as to remove the voltage offset of the capacitors, during both steady state and speed transients. Also, when it comes to references' transients, the experimental results showed that this system features a very fast dynamic response and a total independence in the control of  $i_{sd}$  and  $i_{sq}$ . Besides, the proposed voltage offset suppression control works perfectly and rapidly even when the rotor speed is low, and the speed variations are high.

Additionally, the proposed method was compared to other two previous predictive controls for the TPFSC. The results showed that the proposed predictive control has less low-order harmonics in the stator current and low current THD compared to predictive controls based on one voltage vector and two voltage vectors. In this study, the current THD for predictive control was 3.47%, while for the predictive controls based on one voltage vectors, and two voltage vectors were 10.22% and 5.96%, respectively.

Finally, the highly demanding speed test corresponding to the control of an OWC-based power plant has demonstrated the capability of this control system to follow the maximum efficiency speed reference of the Wells turbine, whereas the capacitor voltage offset was eliminated during the test. As a result, the power plant could keep generating power despite the fault in the power converter. This feature could be also helpful in the case of buoy-based WECs since it allows to keep the control of the buoy in the case of a breakdown in the power converter.

## APPENDIX: NOMENCLATURE

TABLE A1 CHAMBER MODEL

h <sub>c</sub> F				
	Height of the free water surface inside the chamber (m)			
A <sub>c</sub> C	Chamber cross section (m <sup>2</sup> )			
A <sub>d</sub> I	Duct cross section (m <sup>2</sup> )			

Tt	Shaft torque (N.m)	rt	Turbine radius (m).
¢	Flow coefficient (non- dimensional)	$V_{x}$	Air speed in duct (m/s).
$C_t$	Torque coefficient (non- dimensional)	ω <sub>t</sub>	Angular velocity of rotor system (rad/s)
K	Constant of the turbine (kg/m)		

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