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Threshold-variation-enhanced adaptability of response in a nanowire field-effect transistor network

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Stochastic resonance in a summing network with varied thresholds was investigated using GaAs-based etched nanowire field-effect transistors having different threshold voltages. The network's response adapted to input offset fluctuations in the range of the threshold voltage variation and the network could detect a weak signal without any adjustment of the input offset or the addition of high noise. The observed adaptability resulted from a widened dynamic range of the system due to signal decomposition and reconstruction by multiple thresholds together with the output summation process. © 2010 American Institute of Physics. [doi:10.1063/1.3428784]

The coexistence of noise and fluctuations created by utilizing stochastic resonance (SR) has recently been an interesting issue in electronics.^{1–4} SR is a phenomenon in which the response of a system is enhanced by adding noise.^{5,6} It is known to be a key mechanism for precise detection and transmission of weak signals comparable to the thermal energy in biological systems.⁷ To date, many electronic systems that artificially cause SR have been investigated^{8–12} but they have never been turned into practical applications excepting dithering. A major reason is that the SR at an optimal noise never improves the information content of the signal as that before adding noise. However it can improve the information transfer and the signal-to-noise ratio with noise,^{13,14} and this feature is still useful for electronics. We have succeeded in causing SR in a semiconductor nanowire field-effect transistor (FET) and have demonstrated an enhancement of the response of an FET parallel summing network.¹⁵ At this stage, the positive contribution of noise has been confirmed, but the influence of the physical fluctuation of network units on SR has not been clarified, although Collins *et al.*¹⁶ suggested that the noise compensates for the variation in units leading to an optimally enhanced system response. In this Letter, we report on our investigation of the signal response of a summing network of GaAs-based nanowire FETs with threshold voltage variation. The threshold voltages of seven FETs were artificially modified by making devices of different sizes. A parallel summing network was formed using the different FETs and the system's SR response was characterized experimentally.

An FET summing network with varied threshold voltages is schematically shown in Fig. 1(a). The basic mechanism of SR is a noise-assisted state transition in a threshold system.^{6,13} To cause SR, we utilized the nonlinearity of the FET gate threshold characteristic.¹⁵ The input and output of this system were gate voltage V_G and the sum of the source-drain current from each FET $\sum I_{DS}(=I_{out})$, respectively. The FET was operated in the subthreshold region. The FETs were fabricated on nanowires formed by electron beam lithogra-

phy and wet chemical etching of a conventional AlGaAs/GaAs modulation-doped structure with a two-dimensional electron gas. A Schottky wrap gate (WPG) was formed on each nanowire. An example of a fabricated FET is shown in Fig. 1(b). The threshold voltage V_{th} of the present nanowire FET is known to depend on nanowire width W and WPG gate length L_G .^{17–19} We prepared seven FETs having different values of V_{th} by varying W from 290 to 600 nm and L_G from 100 to 690 nm. Measured transfer curves for individual FETs are shown in Fig. 1(c). The obtained V_{th} ranged from +82 to –180 mV, a range of 262 mV. The variation in currents came from the differences in W . To enable us to observe the SR, we applied a common weak pulse train $V_{in}(t)$ to the gates as an input signal. The pulse height was as low as 14 mV, and the frequency and duty ratio were 100 Hz and 20%, respectively. Each gate was biased with a dc offset V_{offset} and uncorrelated white voltage noise was also added to each gate. The noise bandwidth was 50 kHz, which was sufficiently larger than the input signal frequency. Noise was measured as the root mean square (rms) of the voltage noise V_{noise} . I_{out} was obtained by measuring I_{DS} in each FET individually and summing the values on a computer. The system response was

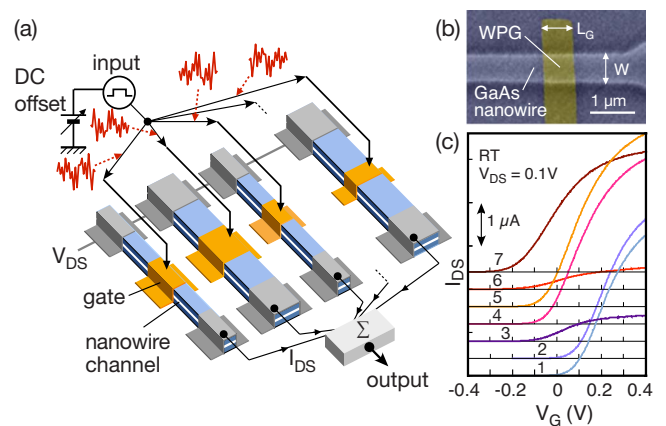


FIG. 1. (Color online) (a) Schematic of nanowire FET summing network with V_{th} variation. The nanowire width and gate length were artificially modulated to achieve V_{th} variation. (b) Scanning electron micrograph of a fabricated FET. (c) Measured transfer characteristics of seven FETs in the network.

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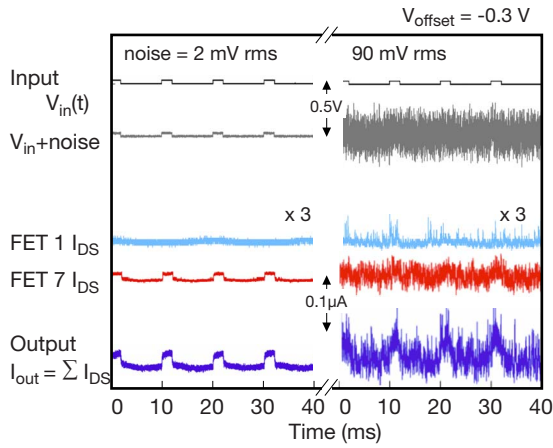


FIG. 2. (Color online) Measured waveforms of the varied FET network when low and high noise was added at $V_{\text{offset}} = -0.3$ V. Outputs of FETs 1 and 7 in Fig. 1(a) are also shown.

evaluated in terms of the correlation coefficient C_1 between input and output waveforms defined by

$$C_1 = \frac{\langle V_{\text{in}} I_{\text{out}} \rangle - \langle V_{\text{in}} \rangle \langle I_{\text{out}} \rangle}{\sqrt{\langle V_{\text{in}}^2 \rangle - \langle V_{\text{in}} \rangle^2} \sqrt{\langle I_{\text{out}}^2 \rangle - \langle I_{\text{out}} \rangle^2}}, \quad (1)$$

where $\langle V_{\text{in}} \rangle$ denotes the ensemble average of $V_{\text{in}}(t)$. It is important to note that this equation gives a meaningful information only when there is no output phase shift.^{14,20} In this study, the input signal frequency was set much lower than the FET cut-off frequency and the noise bandwidth, thus the phase shift could be ignored and we used Eq. (1). In the case with the phase shift, the cross-spectra method is better for evaluation.^{14,20} All measurements were carried out at room temperature. Extrinsic noise in the measurement system was 2.5 mV rms, which was small and did not affect the measurement.

Examples of measured waveforms in the varied FET network when low and high noise was added are shown in Fig. 2. Outputs from two FETs, corresponding to 1 and 7 in Fig. 1(c), are also shown. The responses obtained for individual FETs followed the theory; namely, the response depended on the distance between the input offset and the threshold voltage $\Delta V = V_{\text{offset}} - V_{\text{th}}$.¹⁵ The high- V_{th} FET (labeled 1) showed no response when the noise was low but it began to respond as the noise increased. The low- V_{th} FET (labeled 7) responded to the signal when the noise was rather low. The response degraded as the noise increased and the signal almost disappeared in the output when high noise was added. On the other hand, the behavior of the network was different from the above. The waveforms output from the network obviously showed a pulse train correlated with the input signal regardless of noise intensity. The response observed in the case of high noise was the typical SR of the summing network. A notable feature here was that the present system could respond to the input even when the noise was low.

Figure 3 shows measured input-output correlation coefficients as a function of noise voltage in a 7-FET network with uniform V_{th} of +80 mV and with the varied V_{th} in Fig. 1(c). V_{offset} was changed from -0.4 to $+0.1$ V with a step of 0.1 V. An obvious discrepancy was found between the responses of the two systems. As shown in Fig. 3(a), the uniform FET network exhibited peaked curves when $V_{\text{offset}} \leq -0.2$ V. The peak was higher and broader than that for the

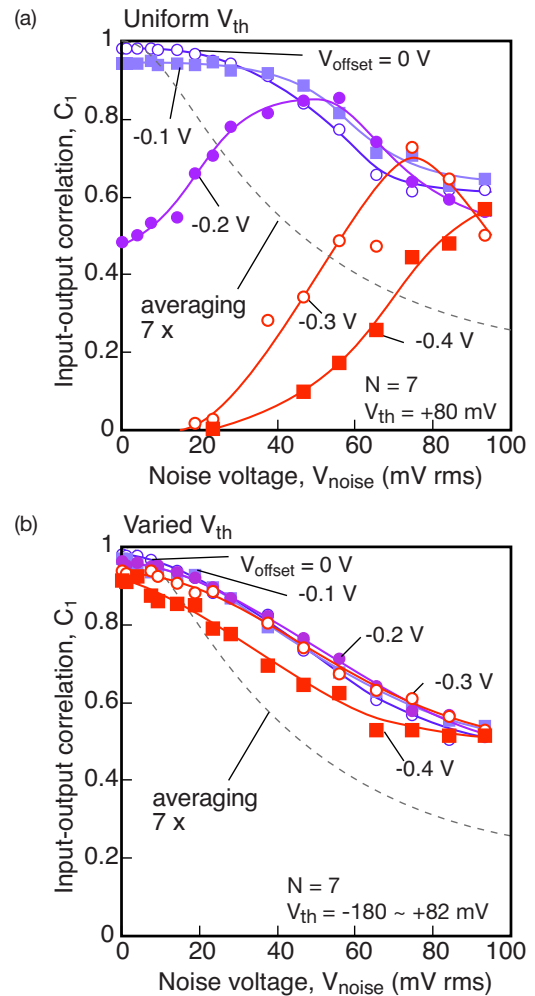


FIG. 3. (Color online) Input-output correlation coefficient as a function of noise voltage in the 7-FET summing network with (a) uniform V_{th} and (b) varied V_{th} for various values of V_{offset} . Broken lines indicate the response of a linear system after $7 \times$ sampling and averaging.

single FET (not shown). These were typical responses of the SR in the summing network.¹⁶ When V_{offset} was increased, the peak position shifted toward the low noise side. Such behavior was the same as that of the single FET and it could be explained by the theory.¹⁵ When $V_{\text{offset}} \geq -0.1$ V, the network showed monotonically decreasing curves. This was because the distance between the threshold and the input offset was short and the peak position was close to $V_{\text{noise}} = 0$ V. However, the correlation coefficient value was still larger than that of the linear system with seven-times sampling and averaging, plotted by dashed curves in Fig. 3. The slight decrease in the high-noise region at $V_{\text{offset}} = 0$ V was due to the weakened nonlinearity of the FET transfer curve, and the response gradually changed to that of the linear system.

The observed response of the varied FET network in Fig. 3(a) was different from that for usual SR. It hardly depended on V_{offset} within the range of V_{th} variation, indicating that the system adapted to the input offset fluctuation. The curve decreased monotonically with increasing noise, and no peak was seen. However, the slope was obviously gentle and the input-output correlation remained high compared with the linear system. These characteristics resulted in increased adaptability to the noise intensity fluctuation in the varied FET network compared with the uniform one. The response curves were similar to those for the uniform FET network in

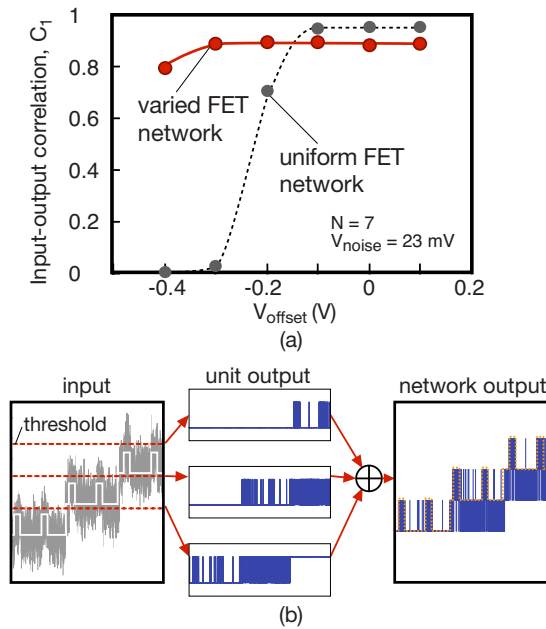


FIG. 4. (Color online) (a) Input-output correlation coefficients as a function of input offset voltage in varied and uniform V_{th} FET summing networks and (b) the mechanism for increased dynamic range of the varied threshold system.

the case of $V_{offset} \geq -0.1$ V. It looked like that the distance between the threshold and the input offset voltages in the present system was always short, even when the offset was shifted.

Correlation coefficients of the uniform and varied FET networks with low noise of $V_{noise}=23$ mV are compared in Fig. 4(a). The varied FET network maintained a high correlation in a wider V_{offset} range than the uniform FET network. This behavior was similar to that obtained in the uniform network when moderate noise was added.¹⁶ This result clarifies that noise in the signal and variation in the threshold contribute similarly to SR. The observed adaptability to the input offset fluctuation and increased sensitivity with low noise in the varied FET network can be explained by an enhancement of the dynamic range due to multiple thresholds and a summation process.²¹ The basic concept is schematically shown in Fig. 4(b). In this system, an input signal distributed over a wide range is decomposed by several units having different thresholds. In the case of a step function with a different threshold for each unit, one unit can detect only part of the input owing to the insufficient dynamic range, as shown in the middle of Fig. 4(b). However, different units separately detect different parts of the input. After that, the input signal is reconstructed simply by summing the decomposed signals, as shown on the right of Fig. 4(b). We found that the varied-threshold network allows the input to cross one of the thresholds when low noise is added. This results in sensitive signal detection in the case of low noise. In addition, the response of the summing network is domi-

nated by the highest unit among all the units. Computing the correlation coefficient in a summing network consisting of linear units for simplicity, we obtain

$$C_1 = \frac{1}{\sqrt{1 + \frac{N}{\left(\sum_{i=1}^N C_{1i} / \sqrt{1 - C_{1i}^2}\right)^2}}}, \quad (2)$$

where N is the number of units and C_{1i} is the correlation coefficient of the i th unit. This formula indicates that the largest C_{1i} in the summing network dominates C_1 of the total system. The above two mechanisms enable the network to achieve high input-output correlation for a wide range of offset and noise intensity fluctuations.

In conclusion, SR in a summing network with varied thresholds was investigated using GaAs-based etched nanowire FETs. The system was found to detect a weak signal without any adjustment of the input offset or the addition of high noise. The improvement in adaptability was explained by a widening of the system's dynamic range. A wide range signal could be detected by decomposing multiple thresholds and reconstructed by summing them. The obtained results clarified that a summing network can exploit the threshold variation in network units to detect a weak signal in the presence of noise.

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