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# Ultrasonic Imaging Transceiver Design for CMUT: A Three-Level 30-V<sub>pp</sub> Pulse-Shaping Pulser With Improved Efficiency and a Noise-Optimized Receiver

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**Abstract**—This paper demonstrates a four-channel transceiver chip for medical ultrasonic imaging, interfacing to the capacitive micromachined ultrasonic transducers (CMUTs). The high-voltage transmitter (Tx) uses a three-level pulse-shaping technique with charge recycling to improve the power efficiency. The design requires minimum off-chip components and is scalable for more channels. The receiver is implemented with a transimpedance amplifier (TIA) topology and is optimized for tradeoffs between noise, bandwidth, and power dissipation. The test chip is characterized with both acoustic and electrical measurements. Comparing the three-level pulser against traditional two-level pulsers, the measured Tx efficiency shows 56%, 50%, and 43% more acoustic power delivery with the same total power dissipation at 2.5, 3.3, and 5.0 MHz, respectively. The CMUT receiver achieves the lowest noise efficiency factor compared with that of the literature (2.1 compared to a previously reported lowest of 3.6, in units of  $\text{mPA} \cdot \sqrt{\text{mW}/\text{Hz}}$ ). In addition, the transceiver chip is tested as a complete system for medical ultrasound imaging applications, in experiments including Tx beamformation, pulse-echo channel response characterization, and ultrasonic Doppler flow rate detection.

**Index Terms**—Capacitive micromachined ultrasonic transducer (CMUT), charge recycling, multilevel pulse shaping, noise efficiency factor (NEF), pulse-echo response, transimpedance amplifier (TIA), transmitter (Tx) efficiency, Tx beamformation, ultrasonic Doppler flow rate detection, ultrasonic transceiver.

## I. INTRODUCTION

ULTRASONIC imaging is an important modality for medical diagnosis. Compared with other imaging modalities, ultrasound is relatively low cost and harmless to human health and has decent resolution. Modern ultrasonic imaging systems are becoming increasingly complex and powerful, yet compact, benefiting from Moore's law [1]. Laptop-size ultrasound systems have gained comparable performance to the traditional cart-size machines; handheld devices, such as the GE Vscan [2], indicates the trend toward highly integrated ultrasonic imaging

solutions to enable portable or even wearable ultrasound applications in hospital and at home.

Piezoelectric transducers (PZTs) have dominated the ultrasonic transducer technology. However, capacitive micromachined ultrasonic transducers (CMUTs) recently emerged as an alternative technology for better system integration [3]. The CMUT technology offers advantages such as improved bandwidth, ease of fabricating large arrays, and potential for integration with electronics with the through-silicon vias (TSVs) [4]–[6] or monolithic CMUT-CMOS integration [7]–[9]. Interfacing to CMUTs is different from interfacing to PZTs, because the effective load is much more capacitive and has higher impedance. The primary reason for the large parasitic capacitance is the physical structure of the CMUT element, which forms a parallel-plate capacitor [10]. As a result, the transmitter and receiver circuitry need to be designed appropriately to prevent excessive performance degradation caused by the large capacitive load.

For the transmitter, high-voltage linear amplifiers are commonly used to drive the PZT loads to achieve good linearity and acceptable efficiency [11], [12]. To drive a CMUT load, however, linear amplifiers are not optimum. In addition to the amplifier power consumption, a considerable power loss becomes associated with charging and discharging the parasitic capacitance of the CMUT element [10], degrading the overall power efficiency of the transmitter stage. Furthermore, the linearity of the amplifier does not translate to good linearity performance of the transmitter stage, because the CMUT element distorts the amplifier's output waveform through the nonlinear relationship between the electrical input signal and electrostatic force acting on the element's membrane [13]. Resonant transmitters with inductors to cancel out the loading capacitances could boost the power efficiency [14]. However, bulky off-chip inductors of several micro-Henries are needed for every transmitting channel, to work with typical loads of 10–200 pF per channel at the ultrasound operating frequency range of 1–20 MHz [15]–[17], which is undesirable for compact integration. Alternatively, the multilevel pulsing technique, which was initially introduced for chip-to-chip interconnects [18], can be applied to reduce the power consumption on the capacitive load. Multilevel techniques have been used in PZT ultrasound drivers for pulse-shaping and harmonic suppression [15]–[17], [19]. However, the power efficiency was not improved because charge recycling was not implemented between the multiple voltage levels. This work [20] presents the advantage of the multilevel pulsing with charge recycling to improve

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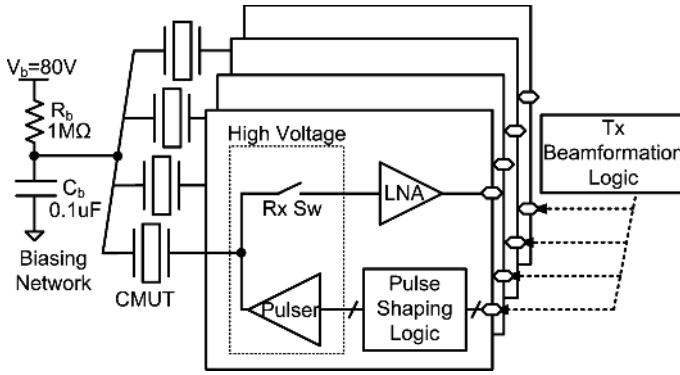


Fig. 1. The transceiver block diagram for medical ultrasound imaging applications using CMUTs.

the combined power efficiency of the CMUT transducer and transmitter.

For the receiver, large input capacitance limits the bandwidth and tends to increase the noise contribution from the input stage transistors, degrading the noise figure. Bulky off-chip inductors are needed to impedance match the source to a traditional PZT pre-amplifier that assumes a low-impedance source [14]. Charge-based amplifiers were attempted for CMUTs. The continuous-time charge amplifier achieved low-noise and low-power performance for CMUT working at kHz range [21], but the large impedance from the dc-setting network limits the bandwidth for a CMUT array working at MHz range for medical imaging applications. The switched-capacitor charge integrating amplifier in [22] could provide enough bandwidth, but issues such as clock feed-through and charge injection are difficult to mitigate for the inherently single-ended CMUT signal path. Moreover, because the sampling clock switches at a higher frequency than input signal bandwidth, the settling requirement for the op-amp demands a higher bandwidth than what is needed in op-amps used as continuous-time buffers, leading to much more power consumption. In this work [20], the transimpedance amplifier (TIA) topology [4], [8], [23] is used to improve the tradeoff between gain, bandwidth and noise, with an inductor-less design at the presence of high input capacitance.

This paper is organized as follows. Section II provides a high-level description of the proposed work. The design methodologies and implementations of the transmitter and the receiver are presented in Sections III and IV, respectively. Section V details the experimental results. The transceiver circuit blocks are characterized and the chip is tested as part of the ultrasonic imaging system for medical applications. Finally, conclusions are drawn in Section VI.

## II. HIGH-LEVEL DESCRIPTION OF THE ULTRASONIC IMAGING TRANSCIEVER CIRCUITS

The block diagram of the ultrasonic imaging transceiver is shown in Fig. 1. All CMUT elements in the array are dc-biased with the shared  $RC$  network provided off-chip. Resistor  $R_b$  and capacitor  $C_b$  filter out noise from the high-voltage supply and provide an ac ground for the transducer. The dc bias voltage  $V_b$  applied on the CMUT is between 50–85 V. The transceiver chip includes four channels. In each channel, a 30-V<sub>pp</sub> high-

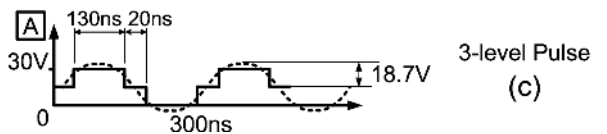
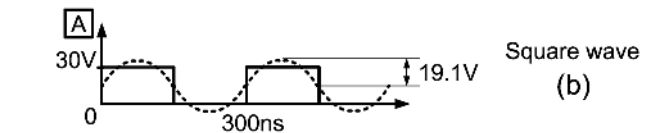
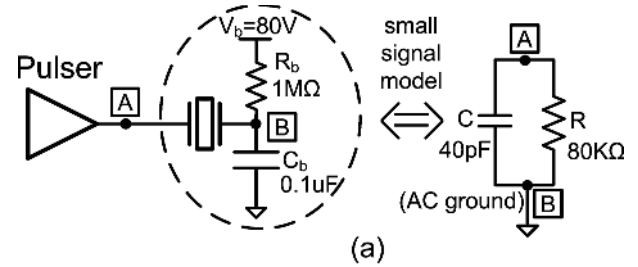


Fig. 2. (a) Small-signal model of a CMUT element used in this work. (b) Exemplary two-level square-wave pulse applied onto CMUT. (c) Exemplary three-level pulse applied onto CMUT.

voltage pulser in the transmitter (Tx) path drives the ultrasound transducer to emit acoustic energy. The emitted ultrasonic wave travels through the medium and is reflected whenever it hits medium boundaries with mechanical impedance mismatch. The reflected echoes are transformed by the CMUT element into a weak electrical signal. A low-noise amplifier (LNA) in the receiver (Rx) path amplifies the weak signal to the output. During transmission, the Rx switch is turned off to prevent the high-voltage transients from breaking the LNA implemented with low-voltage transistors.

After collecting multiple channels' outputs from one or several transmissions, ultrasonic images can be generated. Medical ultrasound systems use beamformation to improve the image quality. Tx beamformation is realized by controlling and applying different delays across the four Tx channels. Similarly, the received signals are digitized and processed by an off-chip Rx beamformer.

## III. TRANSMITTER DESIGN

### A. Multilevel Pulsing for Higher Efficiency

The small-signal model for a CMUT element is represented by a capacitor and resistor in parallel, as shown in Fig. 2(a). The capacitor  $C$  is the parallel-plate capacitance between the CMUT element's membrane and the common node. The resistor  $R$  is the medium's mechanical load at the CMUT surface, transformed to the electrical port [10]. The power dissipated by  $R$ , due to the electrical pulse's fundamental frequency component, models the useful acoustic power delivered into the medium. The power dissipated while charging and discharging  $C$  (dynamic power) does not contribute to the acoustic output and thus is wasted. The CMUT transducer used in this work is a 1-D array. Each CMUT element has a size of  $300 \mu\text{m} \times 3000 \mu\text{m}$  and is modeled as  $40 \text{ pF} \parallel 80 \text{ k}\Omega$  [10].

The Tx efficiency is defined as the ratio between the useful acoustic power and the total power dissipated. It models the

combined efficiency of CMUT and the ultrasonic pulser together by capturing both the power loss in the pulser circuitry and the dynamic power dissipated by charging and discharging the CMUT parasitic capacitance. As discussed in Section I, resonant drivers can be used to recycle the dynamic power and increase efficiency, but off-chip inductors for each channel are needed due to the low operating frequencies. Alternatively, the multilevel pulse-shaping technique with charge recycling reduces dynamic power to increase efficiency [18]. It also requires the least off-chip components, as will be seen in Section III-B.

To show how multilevel pulse-shaping increases Tx efficiency, first assume that conventional two-level square-wave pulses are used to drive a  $40 \text{ pF} \parallel 80 \text{ k}\Omega$  load, as shown in Fig. 2(b). The pulse magnitude is  $30 \text{ V}_{\text{pp}}$  at a frequency of  $3.3 \text{ MHz}$ . The amplitude of the fundamental frequency component is calculated to be  $19.1 \text{ V}$ , or  $13.5 \text{ V}_{\text{rms}}$ . Therefore, the power dissipated on the  $80\text{-k}\Omega$  resistor, i.e., the transmitted ultrasonic power at fundamental frequency, is  $2.28 \text{ mW}$ . Meanwhile, the dynamic power wasted on charging and discharging the capacitor  $C$  is calculated to be:  $CV^2f = 120 \text{ mW}$ .

An  $N$ -level pulser, using  $(N - 1)$  regulated voltage sources to charge and discharge the capacitor in a stepwise fashion, reduces the wasted dynamic power to  $CV^2f/(N - 1)$  [18]. The power saving comes from the charge recycling mechanism during the discharge operation, which is enabled by the regulated voltage supplies<sup>1</sup>. Instead of discarding all the capacitor charge  $CV$  to ground as in the square-wave case, a charge packet of  $CV/(N - 1)$  is recycled back to the power supply when the capacitor is switched from one voltage level to the next lower one. As many as  $(N - 2)$  charge packets of  $CV/(N - 1)$  are recycled until the last packet is dumped to ground. As a result, the dynamic power is reduced by a factor of  $(N - 1)$ . At the same time, the magnitude of the fundamental component is only decreased slightly, leading to overall efficiency improvement. For example, Fig. 2(c) shows three-level pulses with  $20\text{-ns}$  middle voltage level steps, out of a  $300\text{-ns}$  period. Its fundamental frequency component amplitude is  $18.7 \text{ V}$ , or  $13.2 \text{ V}_{\text{rms}}$ . The useful power delivered is  $2.18 \text{ mW}$  and the dynamic power is  $CV^2f/2 = 60 \text{ mW}$ . A comparison with the square-wave example reveals theoretically a  $49\%$  total power saving with only a  $4.4\%$  acoustic power reduction or, equivalently,  $88\%$  more acoustic output power given the same total power dissipation. Measurement results in Section V-A show that this pulse shape achieves maximum efficiency at  $3.3 \text{ MHz}$ , although with less improvement than the theoretical calculation.

### B. Three-Level Pulser Circuit Design

The three-level pulser is implemented in this work, as shown in Fig. 3. The three pulse voltage levels are  $30 \text{ (HVDD)}$ ,  $15$ , and  $0 \text{ V (GND)}$ . The  $15\text{-V}$  middle voltage is generated from a  $2:1$  parallel-series switched-capacitor dc-dc converter (M1–M4), which is shared between channels. The only off-chip components are two  $0.1\text{-}\mu\text{F}$  capacitors. Because of the charge recycling nature of the proposed three-level pulser and the fact that the CMUT load (roughly  $40 \text{ pF}$  per channel) is much smaller than  $0.1 \mu\text{F}$ , the converter can operate at a very low frequency

<sup>1</sup>Without regulated supplies which recycle charge, the dynamic power cannot be reduced even with multilevel pulsing, as is the case in [15]–[17].

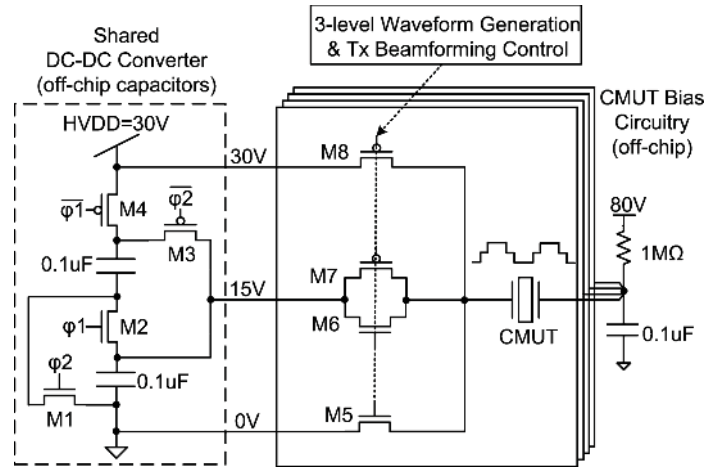


Fig. 3. Circuit schematic of the four-channel three-level pulsers with the middle-voltage generation (all transistors are high-voltage devices).

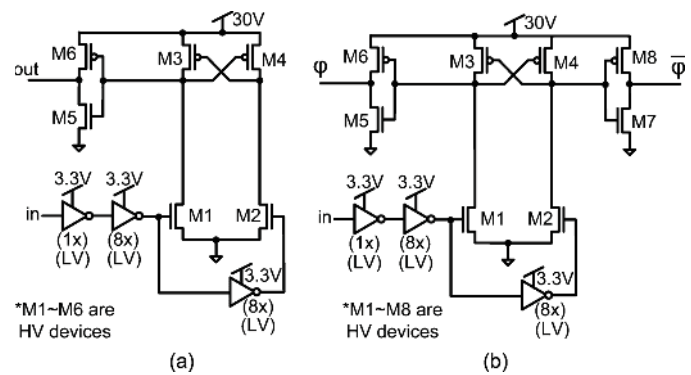


Fig. 4. Level-shifting gate drive for high-voltage transistors in (a) the three-level pulser and (b) the dc-dc converter.

( $10\text{--}100 \text{ Hz}$ ) to save power, consuming less than  $1\%$  of the total four-channel pulsing power.

Three-level pulse-shaping is implemented with four high-voltage switches (M5–M8) in each channel. NMOS M5 and M6 are used for the transitions of  $15 \rightarrow 0 \text{ V}$  and  $0 \rightarrow 15 \text{ V}$ , respectively, while PMOS M7 and M8 are used for the transitions of  $30 \rightarrow 15 \text{ V}$  and  $15 \rightarrow 30 \text{ V}$ , respectively. The on-resistance of each transistor and the CMUT capacitance form an  $RC$  time constant that determines pulse voltage level settling. The transistors are sized to be sufficiently wide to keep the settling time within  $15 \text{ ns}$ , which is less than  $1/20$  of the pulse cycle. The relative timing differences between each channel's control signals is digitally adjustable and effectively implements the Tx beamforming. Low-voltage control signals are supplied off-chip from an FPGA running at  $100 \text{ MHz}$ . Because the signal swing is small, the digital control power is negligible compared to the pulser power. The cross-coupled level shifters in Fig. 4 translates the low-swing signals into high-swing signals that drive gates of the high-voltage transistors in the pulser and the dc-dc converter. The threshold voltage of M1 and M2 is sufficiently low such that they can be completely turned on by the  $3.3\text{-V}$  inverters. The level-shifted gate drive signals have a  $30\text{-V}$  voltage swing, which is under the rated operation conditions of high-voltage transistors in this process.

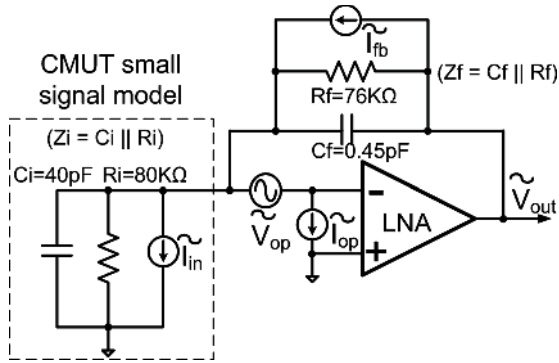


Fig. 5. Small-signal model and noise sources of the CMUT element and the LNA.

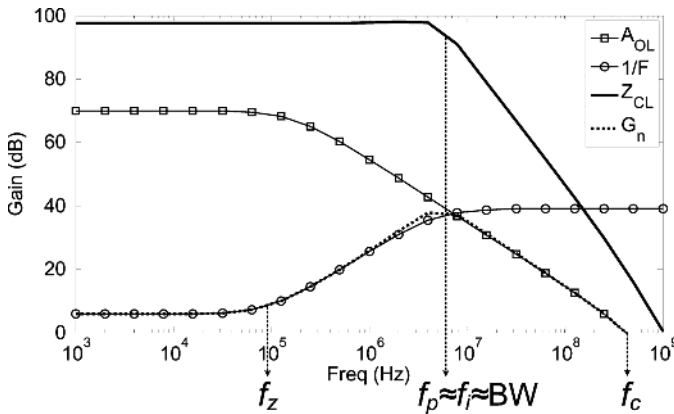


Fig. 6. Transfer functions when the LNA optimality condition is reached.

This design of multichannel pulsers with a shared voltage converter can be extended easily to more Tx channels without additional off-chip components. It could also be revised to implement more voltage levels to achieve more dynamic power reduction. However, this requires the addition of more switches connected between the CMUT and the voltage levels. Due to the large drain capacitance of high-voltage switches, the self-loading effect takes away much of the power savings from introducing additional voltage levels. According to simulation results of the CMOS process used in this work, a three-level pulser dissipates 16% of total power to drive the gate and drain capacitance of M5–M8 in Fig. 3. For a four-level pulser, the dynamic power reduction is counteracted by the power increase to drive more and bigger transistors, leaving the overall efficiency roughly the same as a three-level pulser. A five-level pulser incurs even more power penalty on driving the high-voltage transistors and the efficiency is lower than a three-level pulser.

#### IV. RECEIVER DESIGN

##### A. LNA Optimization Methodology

The transimpedance amplifier topology is chosen to buffer the weak signal out of the high-impedance, highly capacitive CMUT element. Fig. 5 shows the small-signal model of the CMUT and LNA. Figs. 6 and 7 plot various circuit transfer functions to help analyze the optimization process for the LNA.

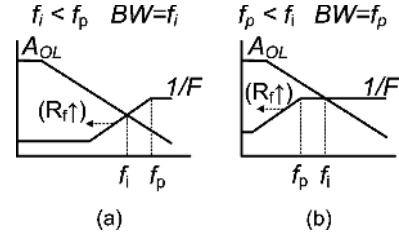


Fig. 7. Transfer function examples when the LNA optimality condition of  $f_i = f_p$  is not reached: (a)  $f_i < f_p$  and (b)  $f_p < f_i$ .

The closed-loop TIA gain is expressed as

$$Z_{CL} = R_f \cdot \left( \frac{1}{1 + sR_f C_f} \right) \cdot \frac{F \cdot A_{OL}}{1 + F \cdot A_{OL}} \quad (1)$$

where  $F = Z_i / (Z_i + Z_f)$  is the feedback factor, and  $A_{OL}$  is the op-amp open-loop gain. From (1), the LNA dc transimpedance gain is  $R_f$  and its bandwidth is determined by the smaller of the following two poles:

$$f_p = \frac{1}{2\pi R_f C_f} \quad (2)$$

$$f_i \approx \sqrt{f_c \cdot f_z} = \sqrt{f_c \cdot \frac{1}{2\pi(R_f || R_i)(C_i + C_f)}} \quad (3)$$

$f_p$  in (2) is due to the  $RC$  time constant of the second multiplying term in (1).  $f_i$  in (3) comes from the third multiplying term in (1), which reaches  $-3$  dB when  $F \cdot A_{OL} = 1$ . Graphically, as can be seen in Figs. 6 and 7,  $f_i$  is the intersection between  $1/F$  and  $A_{OL}$  curves, which is approximately the geometric mean of  $1/F$ 's zero ( $f_z$ ) and the op-amp's unity-gain frequency ( $f_c$ ), assuming a 20-dB/dec slope in both  $1/F$  and  $A_{OL}$  curves.

When  $f_i < f_p$ , as shown in Fig. 7(a), an increase in  $R_f$  always improves LNA's gain-bandwidth product (GBP). This is because gain =  $R_f$ , while bandwidth =  $f_i$ , which is approximately proportional to  $1/\sqrt{R_f}$  as indicated by (3). GBP improves roughly proportionally with  $\sqrt{R_f}$ . However, because  $f_p$  is proportional to  $1/R_f$  as indicated by (2), the increase in  $R_f$  leads to faster decrease in value of  $f_p$  than  $f_i$ . When  $f_i = f_p$ , the LNA achieves maximum GBP available from the op-amp. The phase margin is roughly  $45^\circ$ . Further increase in  $R_f$  no longer improves GBP, because the bandwidth becomes limited by  $f_p$  and is proportional to  $1/R_f$  [Fig. 7(b)], holding the GBP constant. However, as  $R_f$  increases, the phase margin continues to improve at the expense of a reduced bandwidth [23].

The optimality condition  $f_i = f_p$  also minimizes noise contribution from the op-amp input-referred voltage noise. Fig. 5 shows all noise sources in the circuit. The noise figure is expressed as

$$NF = 1 + \frac{R_i}{R_f} + \frac{\tilde{V}_{op}^2}{\tilde{I}_{in}^2 \cdot |Z_i || Z_f|^2} + \frac{\tilde{I}_{op}^2}{\tilde{I}_{in}^2} + \frac{2 \cdot |\tilde{V}_{op} \cdot \tilde{I}_{op}|}{\tilde{I}_{in}^2 \cdot |Z_i || Z_f|} \quad (4)$$

From (4), a large  $R_f$  is desired to reduce its thermal noise contribution. Moreover, the op-amp's input-referred voltage noise ( $\tilde{V}_{op}$ ) has a peaking effect due to the impedance drop in  $|Z_i|$  at higher frequencies. It can be mathematically seen from the

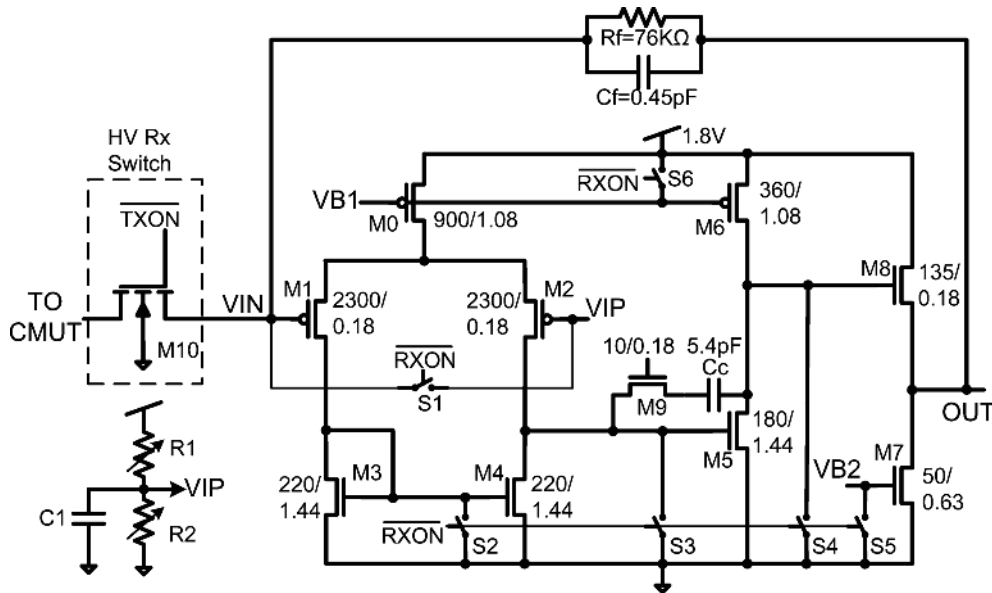


Fig. 8. LNA schematic, implemented in the TIA topology. The transistor sizing is in  $\mu\text{m}$ .

following noise gain expression ( $G_n$ ), defined as the transfer function from LNA input ( $\tilde{V}_{\text{op}}$ ) to the output ( $\tilde{V}_{\text{out}}$ ):

$$G_n = \left| \frac{A_{\text{OL}}}{1 + F \cdot A_{\text{OL}}} \right| = \left| \frac{1}{F} \parallel A_{\text{OL}} \right| \approx \min \left( \left| \frac{1}{F} \right|, |A_{\text{OL}}| \right). \quad (5)$$

The dashed line in Fig. 6 shows the graphical interpretation of (5):  $G_n$  is the lower parts of  $1/F$  and  $A_{\text{OL}}$  curves, which has a considerable peaking effect within the LNA bandwidth. By comparing the optimal and nonoptimal conditions in Figs. 6 and 7, one can see that the condition  $f_i = f_p$  minimizes the noise peaking effect while exploiting the maximum possible GBP from the op-amp design.

### B. LNA Transistor-Level Implementation

Following the guidelines discussed in Section IV-A, the LNA optimization starts with a 5-MHz bandwidth target and the optimal condition:  $f_i \approx f_p \approx \text{BW}$ .  $R_f$  is maximized while keeping the corresponding  $C_f$ , estimated from (2), larger than parasitic capacitances to maintain control over circuit stability. The unity-gain frequency  $f_c$  is estimated from (3) to set the op-amp design target. Further design adjustments keep phase margin above  $60^\circ$ .

Fig. 8 shows the LNA schematic. The input stage devices (M1, M2) are biased at the boundary of strong and weak inversion to achieve high transconductance per unit current and low noise while minimizing size and parasitic capacitance. The differential pair suppresses noise from the power supply and the ground, which is not possible with single-ended topologies [4], [8]. The Miller compensation leg (M9,  $C_c$ ) keeps the op-amp second pole well beyond the closed-loop bandwidth for good phase margin. The source follower (M7, M8) lowers the op-amp output impedance to enforce accurate feedback.

During high-voltage transmissions, the high-voltage Rx switch (M10) is opened and the low-voltage switches (S1–S6) are closed for protection. The on-resistance of M10 directly

impacts LNA noise performance. Its size is chosen such that its noise contribution is only a small portion of the input stage, and its parasitic drain and source capacitance do not degrade phase margin and bandwidth. S1–S6 also implement the op-amp's sleep mode, during which only the reference current remains conducting for fast wake-up within  $1 \mu\text{s}$ . The sleep mode enables system-level power saving opportunities.

## V. EXPERIMENTAL RESULTS

### A. Acoustic Power and Tx Efficiency Measurement

1) *Measuring Acoustic Output Power*: To obtain Tx efficiency, the total pulsing power can be measured electrically. However, the ultrasonic power transmitted into the medium requires acoustic measurements. From

$$P_{\text{acoustic}} = I \cdot A = \frac{p_{\text{rms}}^2}{Z_m} \cdot A \quad (6)$$

acoustic power is the product of the acoustic intensity ( $I$ ) at transducer surface and the transducer surface area ( $A$ ).  $I$  is calculated from the rms fundamental frequency component of the acoustic pressure at the transducer surface ( $p_{\text{rms}}$ ) and the acoustic impedance of the medium ( $Z_m$ ).

In practice, the acoustic pressure at the transducer surface cannot be directly measured. Instead, it can be reliably back-calculated from a pressure measurement at another location. According to [24]–[26], when the transducer aperture is close to a square or a circle, the pressure magnitude profile along the axial direction reaches its maximum at the boundary between the near and far fields. The maximum magnitude is roughly twice the pressure magnitude at the transducer surface.

For the back-calculation, an acoustic pressure measurement system is established in the laboratory, as shown in Fig. 9. The CMUT array is submerged in vegetable oil at the bottom of the oil tank. The test chip circuitry is connected to CMUT from under the oil tank. A hydrophone (ONDA HNC0400)

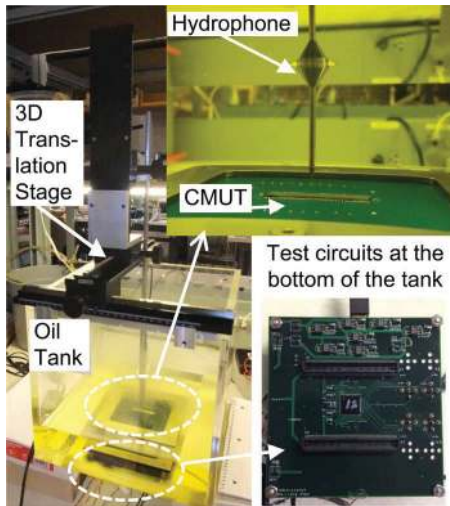


Fig. 9. Photograph of the laboratory setup for measuring the acoustic output power and the Tx efficiency.

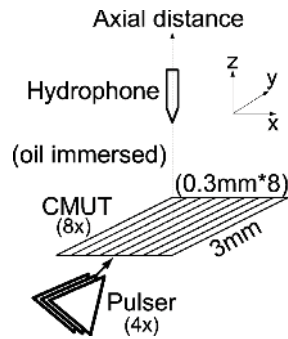


Fig. 10. Acoustic output power and Tx efficiency measurement setup.

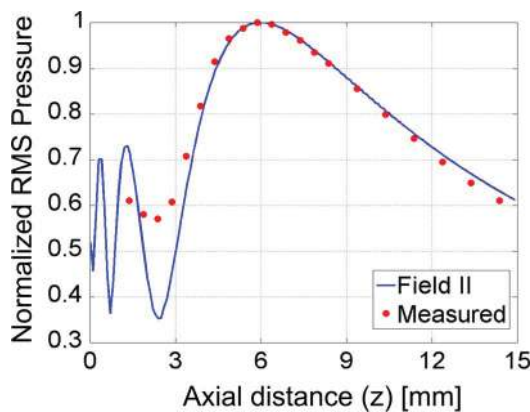


Fig. 11. Normalized rms pressure along the transducer axial axis: measurement versus simulation. The measurement deviates from the simulation in the near field because the hydrophone tip is too close to the transducer surface, distorting the pressure field.

is mounted on the 3-D translation stage to probe the acoustic pressure magnitude generated by CMUT, in the oil medium.

Fig. 10 shows the detailed configuration to measure the acoustic output power. The four-channel pulser circuitry is parallelized and connected to eight CMUT elements in parallel, in order to form an aperture of 2.4 mm × 3 mm (roughly a square). The solid curve in Fig. 11 is the corresponding acoustic simulation of the pressure field using the Field II software [27],

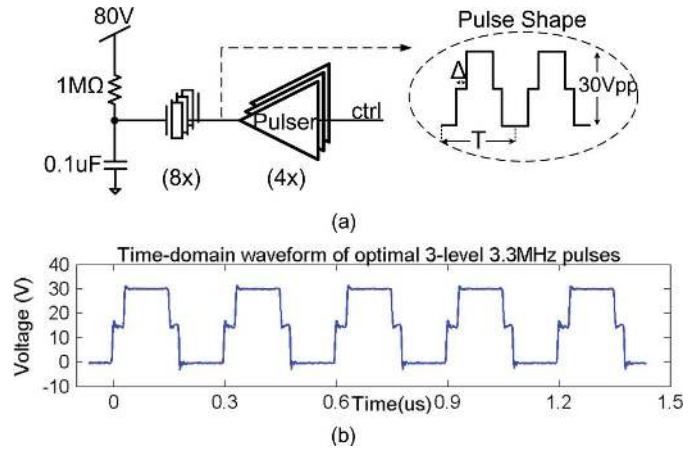


Fig. 12. (a) Tx efficiency measurement setup and pulse shape definition. (b) Measured time-domain waveform of the optimal three-level 3.3-MHz pulses,  $\Delta = 20$  ns,  $\Delta/T = 0.067$ .

[28], verifying that surface pressure ( $z = 0$  mm) is about half maximum pressure ( $z = 5.9$  mm). Furthermore, the hydrophone is used to probe the acoustic pressure magnitude along the axial direction ( $z$ -axis). The measured result in dots in Fig. 11 shows good agreement to both theory and simulation. In the near field, the measured data do not exhibit amplitude fluctuations as predicted by simulation. This is likely caused by the hydrophone tip distorting the pressure field as it approaches the transducer. However, it does not affect the accuracy of the maximum pressure measurement and the surface pressure back-calculation.

2) *Measuring Tx Efficiency:* Fixing the hydrophone at the near- and far-field boundary (5.9 mm away), acoustic output power is obtained with the aforementioned method. Tx efficiency is thus acquired after dividing the acoustic output power by the total power consumption. Different pulse shapes are generated to evaluate the efficiency improvement. The pulse shape is defined by the  $\Delta/T$  ratio, as shown in Fig. 12(a), where  $\Delta$  is the step duration of the middle voltage level and  $T$  is the pulse period. When  $\Delta/T = 0$ , two-level pulses are generated. As  $\Delta/T$  increases, the pulses turn into three-level, reducing the dynamic power from  $CV^2 f$  to  $CV^2 f/2$  and increasing the efficiency. However, as  $\Delta/T$  increases further, the acoustic power starts to decrease because less energy is contained within the pulse shape. Since the dynamic power is kept at  $CV^2 f/2$ , efficiency decreases. Therefore, there is an optimal pulse shape to maximize the Tx efficiency. For example, Fig. 12(b) is a time-domain waveform of optimal three-level pulses at 3.3 MHz.

Fig. 13 shows the measurement results. As an example, Table I(A) compares the optimal three-level pulser against the two-level pulser operating at 3.3 MHz: the optimal three-level pulser dissipates 38% less total power at the cost of delivering 7% less acoustic power. In other words, the three-level pulser outputs 50% more acoustic power at the same power dissipation. The measured improvement is not as big as the theoretical calculation in Section III-A (50% rather than 88%), mainly for two reasons. First, the RC settling transition distorts pulse shapes, with three-level pulses being distorted more severely than two-level pulses, which leads to more acoustic power reduction in a real-world three-level pulser (7% rather than

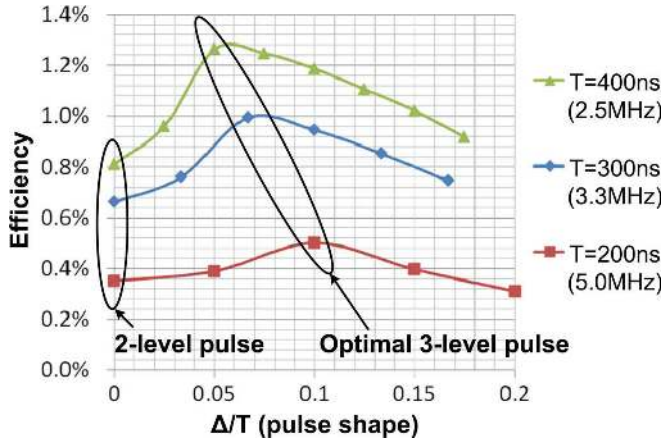


Fig. 13. Tx efficiency measurement results using different three-level pulse shapes by varying the  $\Delta/T$  ratio and at different frequencies.

TABLE I

MEASURED PULSER PERFORMANCE SUMMARY AND COMPARISONS. (A) POWER AND EFFICIENCY COMPARISON AT 3.3 MHz. (B) OPTIMAL THREE-LEVEL PULSER PERFORMANCE SUMMARY

A			
	2-level	Optimal 3-level	Change
Acoustic Power	0.56mW	0.52mW	-7%
Total Power	84.5mW	52.4mW	-38%
Efficiency	0.66%	1.0%	50%

B			
	2.5MHz	3.3MHz	5.0MHz
Total Power (mW)	39.4	52.4	77.6
Relative Efficiency Improvement Against a 2-level Pulser	56%	50%	43%

4.4%). Second, a three-level pulser uses more high-voltage transistors than a two-level pulser, dissipating more power for driving the transistors' gate and drain capacitance, which leads to less total power reduction (38% rather than 49%).

The relative efficiency improvements of a three-level pulser over a traditional two-level pulser at 2.5-, 3.3-, and 5.0-MHz pulses are measured to be 56%, 50%, and 43%, respectively. Table I(B) lists the optimal 30-V<sub>pp</sub> three-level pulser power dissipation and efficiency at all three measured frequencies. Efficiency improvement is less for pulses with a shorter period, because the same  $RC$  settling transition distorts shorter pulse shape more severely, reducing useful acoustic output power. Moreover, higher frequency pulses dissipate proportionally more dynamic power while acoustic output power is kept roughly the same, thus the overall efficiency curve shifts down. Lastly, the optimal  $\Delta$  value for the three frequencies is approximately the same (20 ns), which is slightly more than the  $RC$  settling time. This is because the optimal pulses use just enough time to settle to the middle level to achieve  $CV^2f/2$  dynamic power, while keeping the middle level as narrow as possible to maintain large fundamental frequency pulse energy delivery. When normalized over pulse period  $T$  in Fig. 13, the optimal  $\Delta/T$  ratios become different for different pulse frequencies.

By comparing the optimal three-level pulser against the two-level pulser, this work is effectively compared against a range of traditional pulsers. The reason is that, not only for two-level pulsers [4], [10], [29], but also for multilevel pulsers without

charge recycling [15]–[17] or pulsers implemented as linear amplifiers [11], [12], the dynamic power dissipation is always  $CV^2f$ . Therefore, these traditional pulsers have similar (if not worse, considering the quiescent power dissipation in linear amplifiers) Tx efficiency performance as the two-level pulser used in this work.

### B. Tx Beam-Steering Experiment

The ultrasonic lateral beampattern can be measured based on slight modification of the experimental setup in Fig. 9. In this experiment, each of the four-channel pulsers is connected to one of four consecutive CMUT elements; each pulser drives its CMUT with the 3.3-MHz optimal three-level pulses. The hydrophone is placed at a fixed depth in the transducer's far field ( $z = 7.4$  mm). By moving the hydrophone along the lateral direction ( $x$ -axis) and collecting the acoustic pressure readings, the lateral beam profile can be plotted. Furthermore, ultrasonic Tx beam-steering is demonstrated on the four-channel transmitter system when varying the relative pulsing delays across four channels.

Fig. 14(a) shows the measured beam profile in dots with zero delay between channels. The beam is steered to the center, i.e., broadside. Fig. 14(b) shows the profile when 30-ns delay is applied between channels. The figures also show the Field II simulation results of the same experimental configurations for each case. The simulation and measured data match well. Hand calculations based on classical wave propagation provide another verification for Fig. 14(b). The beam lateral displacement  $\Delta x$  and the channel delay,  $t_d = 30$  ns, are related to each other by

$$\frac{\Delta x}{z} \approx \frac{t_d \cdot c}{d} \quad (7)$$

where depth  $z = 7.4$  mm, sound speed  $c$  in vegetable oil is measured to be 1460 m/s, and CMUT element pitch  $d = 300$   $\mu$ m. The calculated beam lateral displacement  $\Delta x = 1.08$  mm, which is consistent with the measured result.

### C. LNA Characterization

The LNA is first tested as a single amplifier block, and Table II summarizes its performance. The LNA sleep power is measured when all LNA biasing currents are kept on to achieve fast wake-up time. In Table III, the LNA is compared against other CMUT LNAs in the literature.

Being used for different medical ultrasound applications, the CMUT arrays are very different in size, impedance and operating frequency. Thus, the corresponding LNA specs are also vastly different and difficult to compare.<sup>2</sup> To establish a figure of merit for fair comparison and to be able to apply the data available in CMUT LNA literature, the noise efficiency factor (NEF) commonly used for instrumentation amplifiers [30] is revised for use in this paper. The revised NEF' is expressed in

$$\text{NEF}' = \frac{V_{\text{rms,in}}}{\sqrt{\text{BW}}} \cdot \sqrt{P_{\text{tot}}} = V_{n,\text{in}} \cdot \sqrt{P_{\text{tot}}} \quad (8)$$

where  $V_{\text{rms,in}}$  is the input-referred rms noise amplitude in-band and  $V_{n,\text{in}}$  is the input-referred noise spectral density averaged inside the passband. Note that both  $V_{\text{rms,in}}$  and  $V_{n,\text{in}}$  are acoustic pressure noise, input-referred all of the way to the

<sup>2</sup>For example, the CMUT used in this work is designed as an alternative to 1-D PZT linear arrays operating at 2–5 MHz. The LNA bandwidth target of DC–5 MHz is set accordingly.



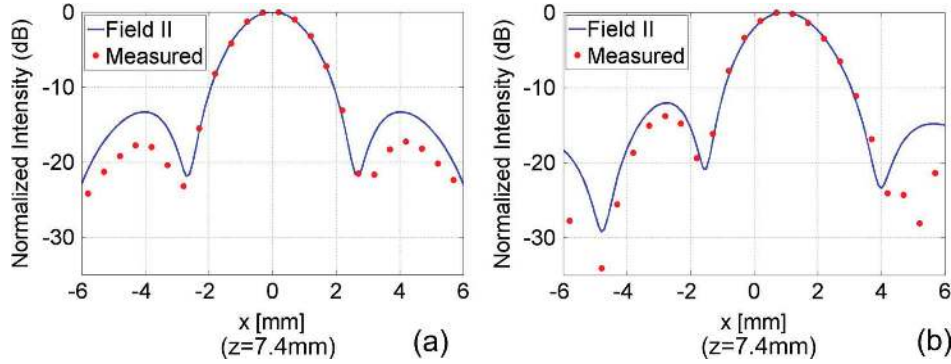


Fig. 14. (a) Measured ultrasonic lateral beam profile, steered to the center (broadside). (b) Measured beam profile, with 30-ns delay between channels.

TABLE II  
MEASURED LNA PERFORMANCE SUMMARY

LNA Specs	Measured Result
Process	0.18 $\mu$ m CMOS
Target CMUT Area	300 $\mu$ m x 3000 $\mu$ m
Active Power Consumption	14.3 mW
Sleep Power Consumption	1.5 mW
Bandwidth	5.2 MHz
Transimpedance Gain	96.6 dB $\Omega$
Receive Sensitivity	162 mV/kPa
Input-referred Noise	0.56 mPa/ $\sqrt{Hz}$ @3MHz
Output-referred Noise	91 nV/ $\sqrt{Hz}$ @3MHz
Noise Figure	10.3 dB@3MHz
Output P1dB	618 mVpp@3MHz
4-Ch Gain Mismatch	<0.11 dB $\Omega$
Crosstalk	<-47 dBc@3MHz; <-35 dBc@10MHz
Wake-up/Sleep Time	<1 $\mu$ s

mechanical side at the CMUT element surface, in the unit of Pa and Pa/ $\sqrt{Hz}$ , respectively. This input-referred method normalizes the effect of CMUT receive sensitivity and LNA gain. Moreover, the input-referred noise spectral density at the center frequency of the passband is used to approximate  $V_{n,in}$  (the input-referred noise spectral density averaged inside the passband) for NEF' calculation, because it is the more accessible measurement result in the literature.

The NEF' in (8) handles CMUT element size scaling correctly. For example, a CMUT element with  $2\times$  bigger surface area presents approximately  $2\times$  bigger input capacitance to the LNA. If two of the same LNAs are parallelized to buffer the  $2\times$  CMUT element, the same bandwidth and noise figure targets are achieved. Although the parallelization reduces the input-referred noise amplitude by  $\sqrt{2}x$  and increases the power consumption by  $2\times$ , the NEF' is held unchanged. This is expected since the same LNA design is used in both cases. Another example to show the usefulness of NEF' is [8] in Table III. It achieves a very low noise performance, as indicated by the noise figure. On the other hand, excessive power is dissipated on a very small CMUT element, which leads to a relatively high NEF'.

Table III suggests that the LNA design in this work achieves the lowest NEF', indicating the best power efficiency for noise and bandwidth performance. In addition, this design achieves a good linearity performance as shown by the P1dB numbers.

#### D. Pulse-Echo Experiment

The LNA measurement is followed by the pulse-echo experiment, which characterizes the complete ultrasound signal chain. The test setup in Fig. 9 is revised to perform the experiment. As shown in Fig. 15, the pulser drives a single CMUT element with a wideband pulse as an approximation to the ideal impulse excitation. The narrowest pulse that can be generated from the pulser is a two-level 30-Vpp pulse with 20-ns pulse width [Fig. 16(a)]. The excited ultrasonic wave then propagates through the oil medium and is reflected back at the oil-air boundary 26 mm away from the transducer (the hydrophone is not needed for this experiment). The reflected echo is received by the same CMUT element and amplified by the LNA [Fig. 16(b)]. Because the CMUT blocks the dc component and acts as a differentiator, the received echo looks similar to the derivative of the transmitted pulse, with a positive peak and a negative peak corresponding to the rising edge and the falling edge of the transmitted pulse. Moreover, the pulse's high frequency components get attenuated more, leading to the dispersed received echo. The echo duration is about 0.3  $\mu$ s, corresponding to the dominant frequencies (3–5 MHz) that go through the ultrasound signal chain. The echo's FFT in Fig. 16(c) confirms the intuition. It shows the total channel impulse response, including CMUT, the oil medium, and LNA. It mainly reflects the bandpass characteristic and the wide bandwidth of the CMUT device, with a center frequency of 4.5 MHz and a  $-6$ -dB fractional bandwidth of 116%.<sup>3</sup>

#### E. Ultrasonic Doppler Experiment for Volumetric Flow-Rate Detection

1) *Test Setup*: The ultrasonic Doppler experiment utilizes the transceiver system to demonstrate the volumetric flow rate detection. In order to generate the flow for the experiment, a water circulation system is assembled in the laboratory, as shown in Fig. 17. The system includes a centrifugal pump, a pressure gauge, a check valve, a needle valve, a flow meter, and a water reservoir. One segment of a rubber tube with the circulating water is placed above the CMUT array inside the oil tank, tilted at  $45^\circ$ , so that the ultrasonic transmitter can emit ultrasonic power into the tube and the receiver detects the scattered Doppler signal from the flowing water for further Doppler processing to estimate the flow rate.

<sup>3</sup> $-6$ -dB bandwidth is used instead of  $-3$  dB because the spectrum is showing the combined CMUT characteristic both ways.

TABLE III  
CMUT LNA PERFORMANCE COMPARISON

LNA Specs	This Work	[36] (2010)	[4] (2008)	[8] (2011)	[29] (2005)
Target CMUT Area ( $\mu\text{m} \times \mu\text{m}$ )	300 x 3000	63 x 1037	250 x 250	70 x 70	200 x 200
Power [ $P_{tot}$ ] (mW)	14.3	3.81	4.0	6.6	2.0
Bandwidth (MHz)	5.2	20	10	10~20	1.2~3.5
TIA Gain (dB $\Omega$ )	96.6	94.0	112.7	129.5	N/A
Input-referred Noise [ $V_{n,in}$ ] ( $\text{mPa}/\sqrt{\text{Hz}}$ )	0.56@3MHz	2.18@10MHz	1.8@5MHz	3.0@15MHz	12@2MHz
Noise Figure (dB)	10.3@3MHz	10.5@10MHz	N/A	1.8@10~20MHz	N/A
Output P1dB (mVpp)	618@3MHz	84.2@8MHz	N/A	N/A	N/A
NEF [ $V_{n,in} \cdot \sqrt{P_{tot}}$ ] ( $\text{mPa} \cdot \sqrt{\text{mW}/\text{Hz}}$ )	2.1	4.2	3.6	7.7	17

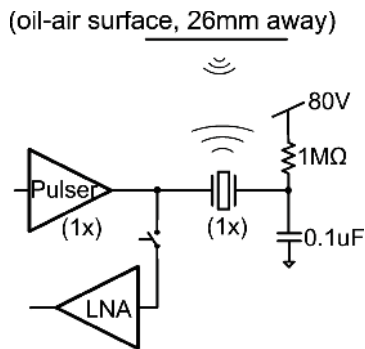


Fig. 15. Setup of the pulse-echo experiment for characterizing the complete ultrasound channel.

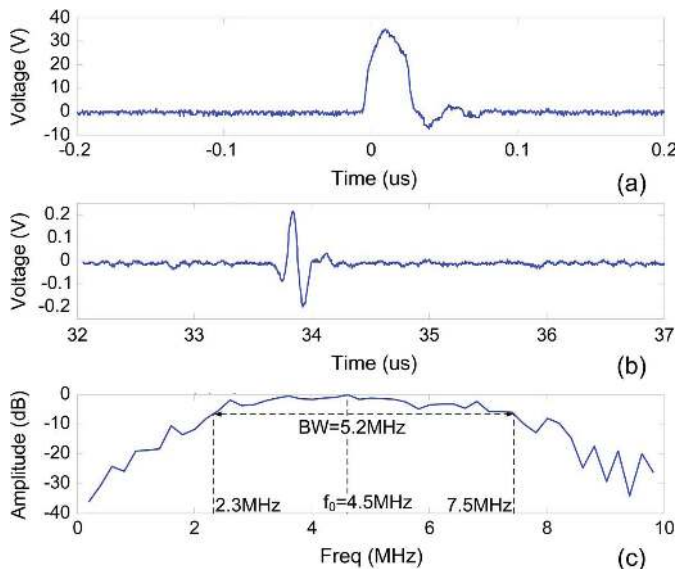


Fig. 16. Key waveforms from the pulse-echo experiment, showing the ultrasound channel characteristics. (a) Transmitted pulse waveform. (b) Received echo waveform. (c) Spectrum of the received echo waveform.

The test setup is used to mimic the scenario of detecting human blood flow rate inside a main artery, such as the common carotid artery. The vegetable oil in the oil tank is similar to human body fat in acoustic property, and the inner diameter of the rubber tube is chosen to be a quarter inch (6.35 mm) and is close to the average common carotid artery diameter of an adult person [31]. The water flow mimics the blood flow, and the air bubbles in the water mimics the blood cell, which is

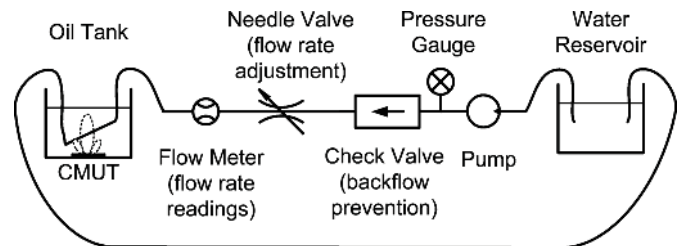


Fig. 17. Flow system setup for the ultrasonic Doppler experiment.

responsible for scattering the incoming ultrasonic wave generated from the transmitter. The water flow rate is tunable with a needle valve between 0.32–2.52 L/min and the actual volumetric flow rate is measured by the flow meter as a reference. The volumetric flow rate range of 0.32–2.52 L/min corresponds to a flow speed range of 0.17–1.33 m/s inside the quarter-inch rubber tube, covering enough range between the end-diastolic minimum velocity (0.2 m/s) and the peak-systolic maximum velocity (1.0 m/s) in human common carotid artery [32].

2) *Doppler Processing Principle*: The classical continuous-wave (CW) Doppler system emits a continuous sinusoidal ultrasonic wave; the receiver detects the frequency shift in the reflected wave due to moving particles, which is proportional to the velocity. Because the continuous wave does not tell the traveled distance before reflected back to the transducer surface, CW systems lack range differentiation [33], [34].

The pulsed-wave (PW) Doppler mode is more widely used in medical ultrasound for its better range resolution. PW systems apply short pulses and can resolve flow information at different depths at the same time. However, the classical Doppler frequency shift cannot be detected in PW mode because the received echoes are of short duration and wideband. Instead, it is through detecting slight timing differences in the received echoes across multiple pulse transmissions, with either subsampling or autocorrelation methods, that the flow information can be extracted [33], [35]. Equivalently, the processing can be intuitively explained from the frequency domain. The spectrum of a train of echoes spaced out at a fixed pulse repetition period (PRP) is a sampled version of a single echo's spectrum. The spacing of the frequency sampling is the pulse repetition frequency (PRF = 1/PRP). If the scene is not moving, the resultant echo train is steady, and all peaks in its spectrum are located at multiples of the PRF. In contrast, the echo train coming off a moving scene would have slight timing variations between

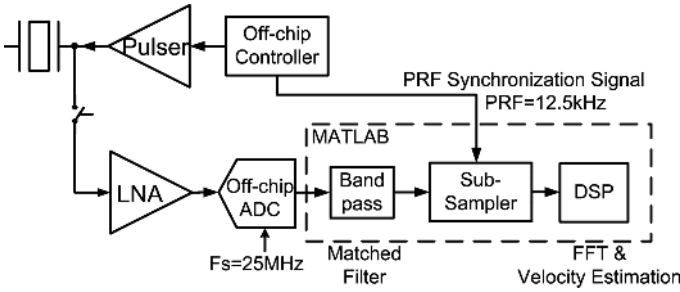


Fig. 18. Block diagram of the PW Doppler processing signal chain.

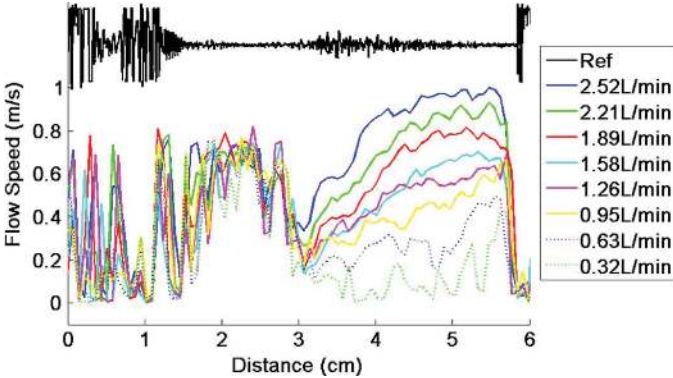


Fig. 19. Ultrasonic Doppler flow speed measurement result. The flow speed is detected at different distances.

consecutive echoes, leading to small shift of peaks in the spectrum. When the echo train is subsampled or autocorrelated at the exact same PRF rate, all spectrum peaks are aliased to baseband (0-PRF) in the frequency domain. For a still scene, all peaks add up at dc. However, for a moving scene, the peaks accumulate at a frequency that is proportional to the velocity in the scene, hence the ‘‘Doppler shift’’  $\Delta f$ . The velocity  $v$  can be calculated from

$$v = \frac{c \cdot \Delta f}{2 \cdot f_0 \cdot \cos(\theta)} \quad (9)$$

where  $c$  is the sound speed,  $f_0$  is the pulse frequency, and  $\theta$  is the angle between the flow direction and the ultrasonic beam direction. Rigorous derivations of the PW Doppler principle are found in [33] and [35].

3) *Experimental Results:* A subsampling-based, PW Doppler flow detection system is implemented in this work, as shown in Fig. 18. The four-channel transceiver chip is driving four consecutive CMUT elements in parallel; five cycles of 3.3-MHz three-level pulses are used. The subsampler down-samples a 25-MHz (ADC sampling rate) data stream of the echo train into a 12.5-kHz (PRF) data stream. The DSP block performs FFT and velocity estimation. The time difference between the subsampling instance and beginning of each repetition cycle, i.e., the pulse transmission, represents the depth where the Doppler information is estimated. The subsampling instance can be adjusted through the PRF synchronization signal, such that all depths can be swept at the same time for Doppler information.

The Doppler measurement result is shown in Fig. 19. At the top of the figure, a typical received ultrasound echo waveform is plotted as a reference. As the waveform progresses in time, it

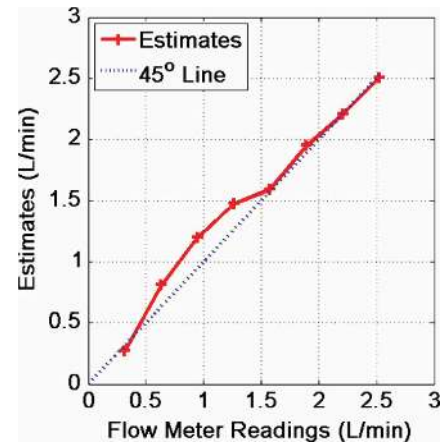


Fig. 20. Ultrasonic Doppler volumetric flow rate estimations as compared with the reference readings from the flow meter.

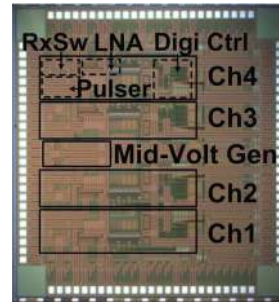


Fig. 21. Die photograph of the four-channel ultrasonic imaging transceiver test chip.

carries information about the acoustic properties of the material at the corresponding distance. Therefore, the  $x$ -axis of the figure can be interchangeably labeled in time or distance, which are related to each other by

$$\text{Distance} = c \times \text{Time}/2 \quad (10)$$

where the factor of 2 represents the fact that the pulse travels twice the distance to reach the medium and come back. As indicated by the reference echo waveform, high-voltage pulses are transmitted at transducer surface (Distance = 0 cm). At roughly Distance = 3 cm, the pulse reaches the front rubber tube wall, exciting visible stationary reflections. After getting through the tube wall, the ultrasound pulse wave is scattered by the moving air bubbles. The scattered echoes are amplified by the receiver and processed, leading to the flow velocity estimations between Distance = 4–5 cm. The different estimated velocity profiles from different flow rates can be clearly differentiated in Fig. 19.

By integrating each estimated flow velocity profile in Fig. 19 across the tube diameter, the volumetric flow rate can be calculated. Fig. 20 shows the calculated volumetric flow rates compared against the actual readings from the flow meter. The estimations match the readings quite well.

There are several error sources in this experiment. First, the window placement for the velocity profile integration could be slightly different than the actual rubber tube position in the space. Second, the ultrasonic Doppler estimation detects the instantaneous flow velocity within the data collection duration, while the flow meter reading is the time averaged flow rate.

Since the flow generated by the centrifugal pump is not perfectly steady and constant, the estimated instantaneous flow rates could deviate from the average flow rate readings.

Fig. 21 shows a die photograph of the test chip fabricated in TSMC 0.18- $\mu\text{m}$  high-voltage CMOS process. Each channel occupies an area of  $300\ \mu\text{m} \times 1100\ \mu\text{m}$ . The shared middle voltage generation circuit occupies an area of  $300\ \mu\text{m} \times 600\ \mu\text{m}$ .

## VI. CONCLUSION

This paper describes a transceiver for compact low-power medical ultrasonic imaging applications. The multilevel pulse-shaping technique is successfully applied to the CMUT transmitter for increased power efficiency than traditional designs. The transmitter requires minimum off-chip components and can be easily extended to more channels. The receiver is implemented with the transimpedance amplifier topology in the presence of a highly capacitive source. The LNA achieves the best power-noise tradeoff compared with other works. Furthermore, the combined electrical and acoustic measurements demonstrate complete system functionality. Not only the performance of individual blocks are measured, the test chip is also used as part of a ultrasonic imaging system to perform experiments including ultrasonic beamformation, pulse-echo channel characterization, and PW Doppler flow rate detection.

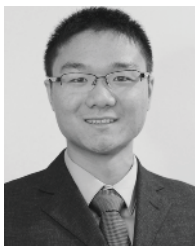
## ACKNOWLEDGMENT

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